

**Compal Confidential**

**ELY5U UMA MB Schematic Document**

**CFL-H Refersh Processor and PCH-H with DDR4 Memory Down**

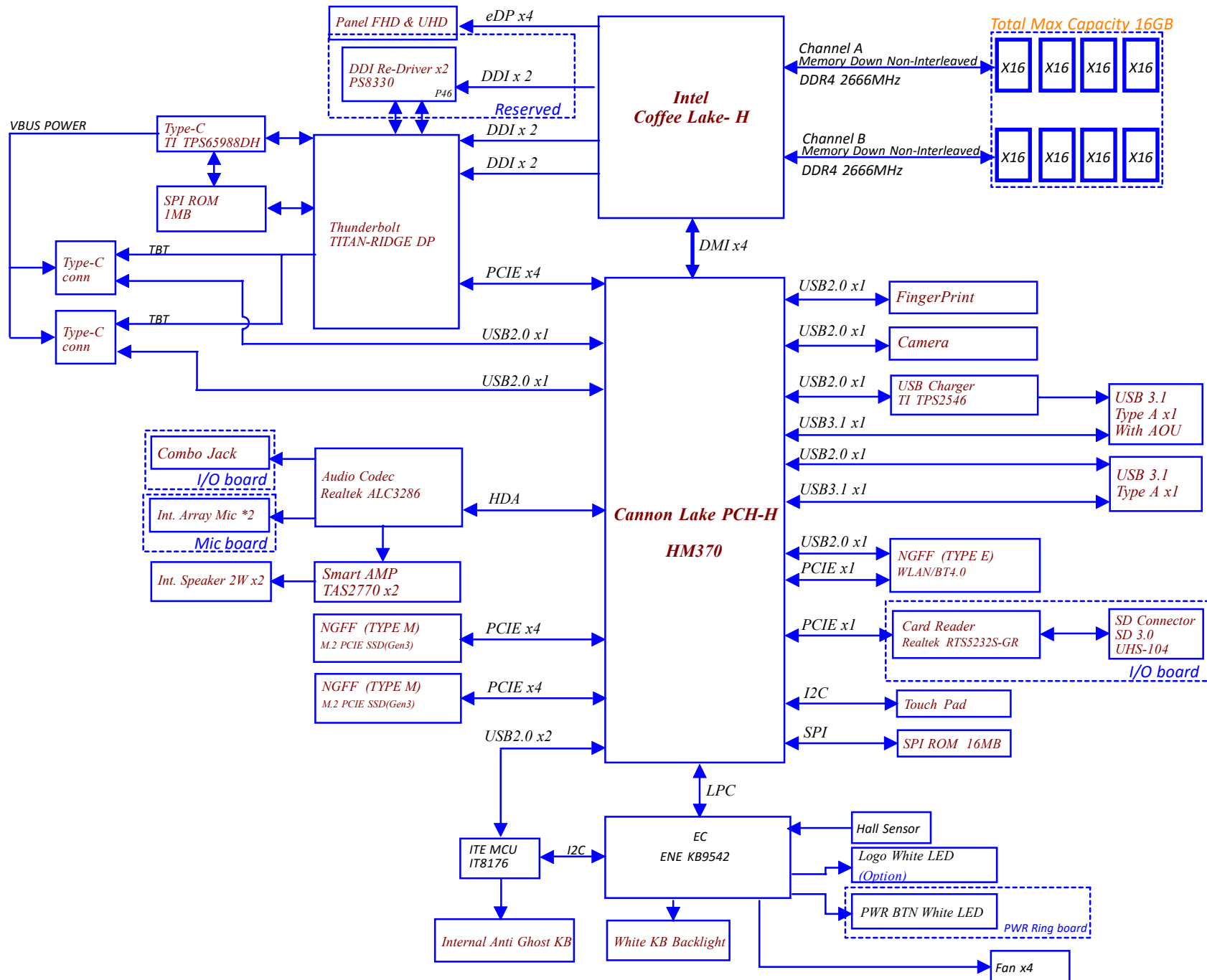
**LA-J061P**

**Rev: 0.3**

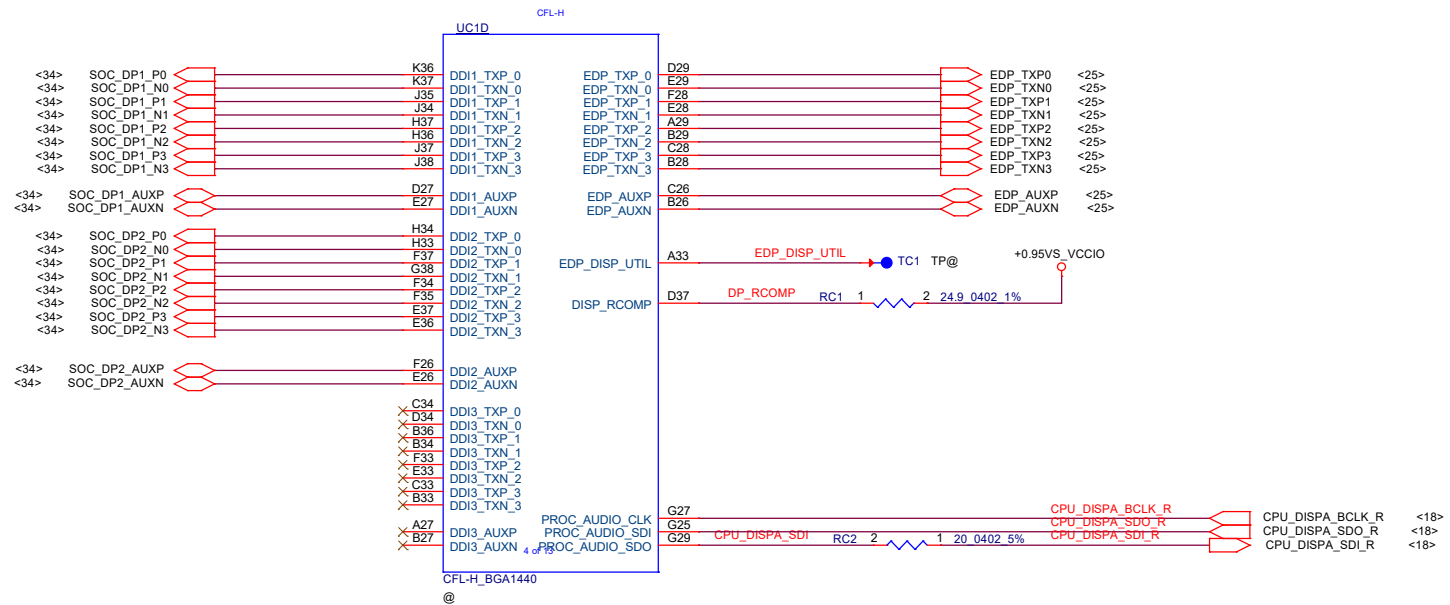
**2019.7.2**

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# Coffee Lake- H



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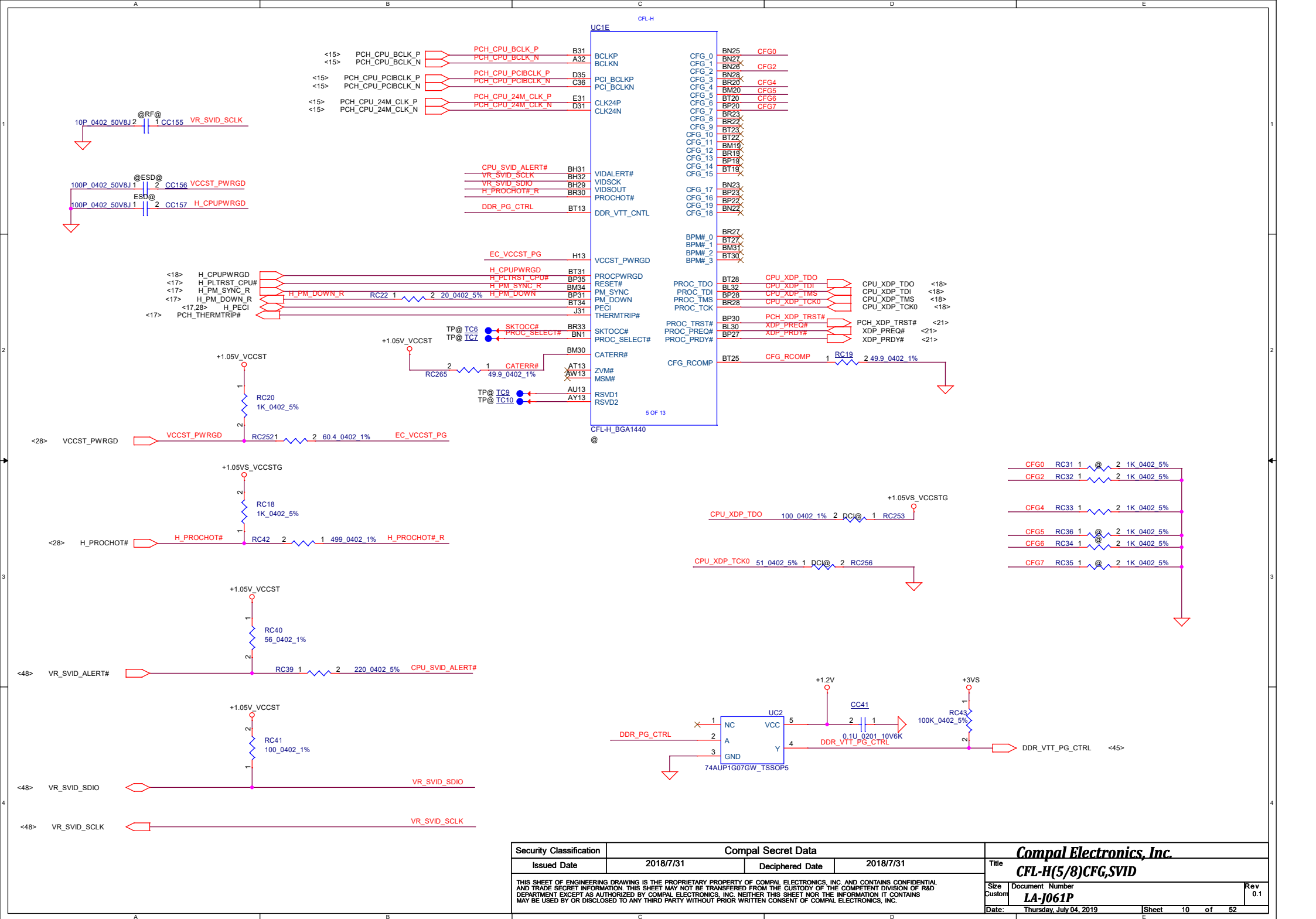


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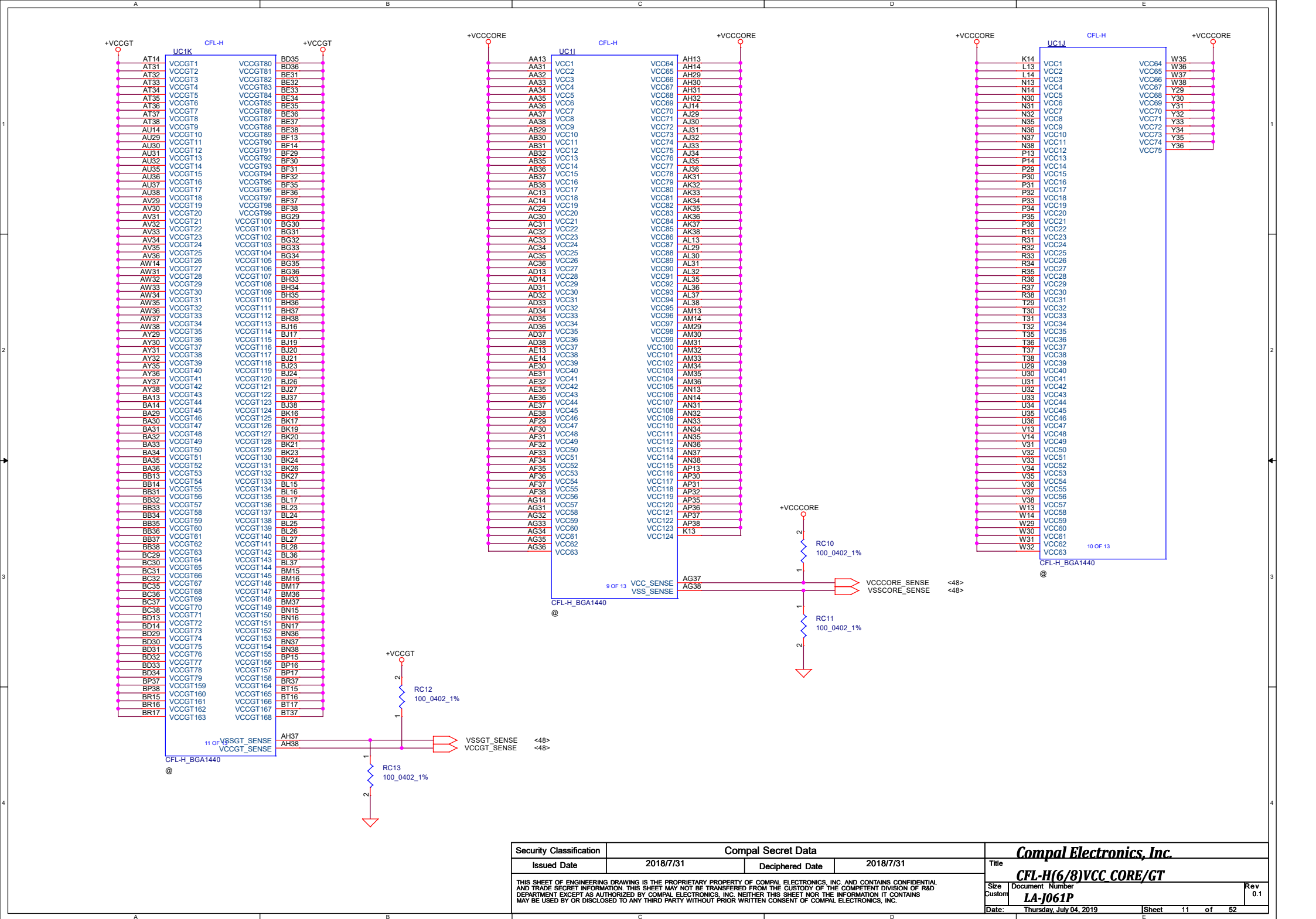




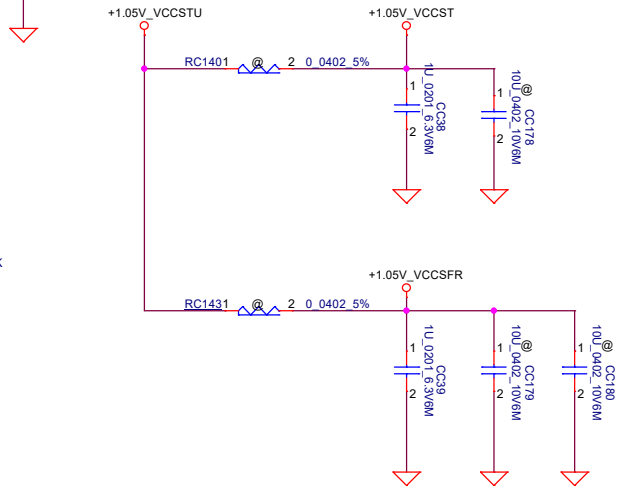
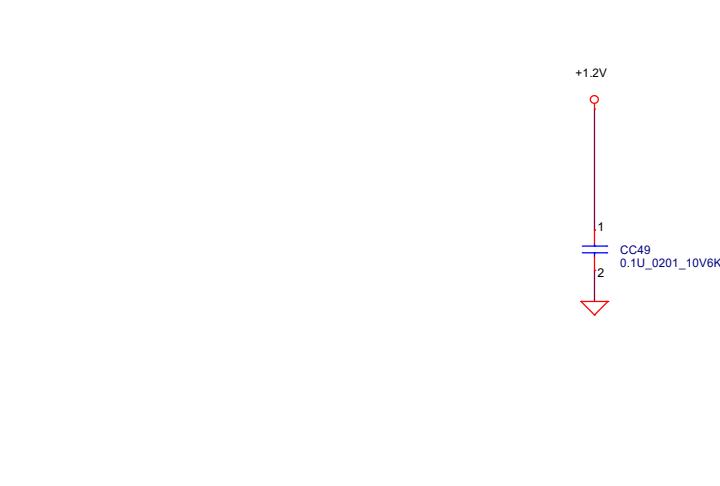
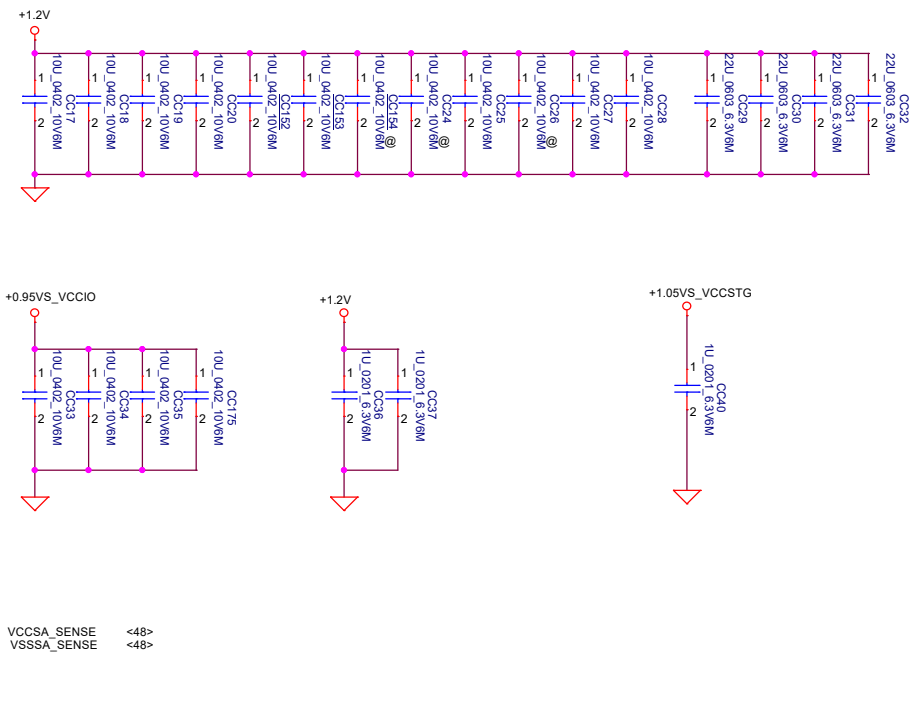
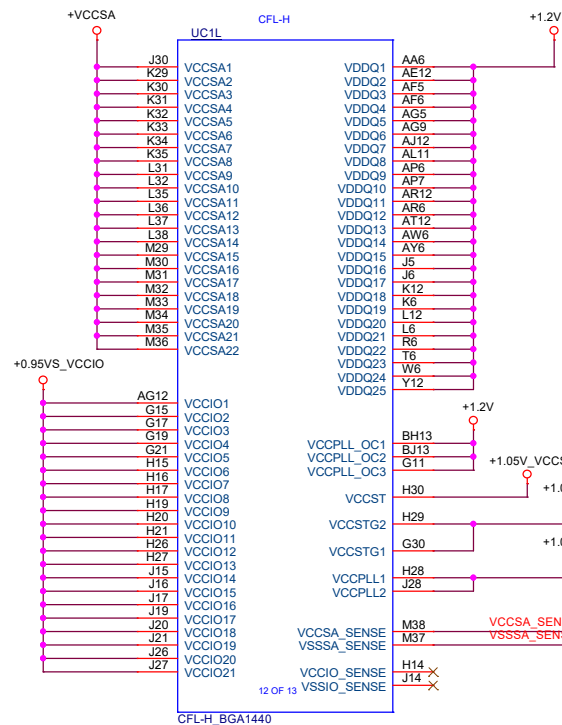




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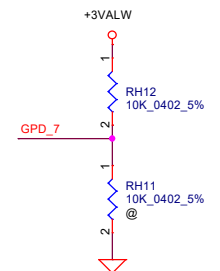
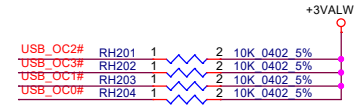




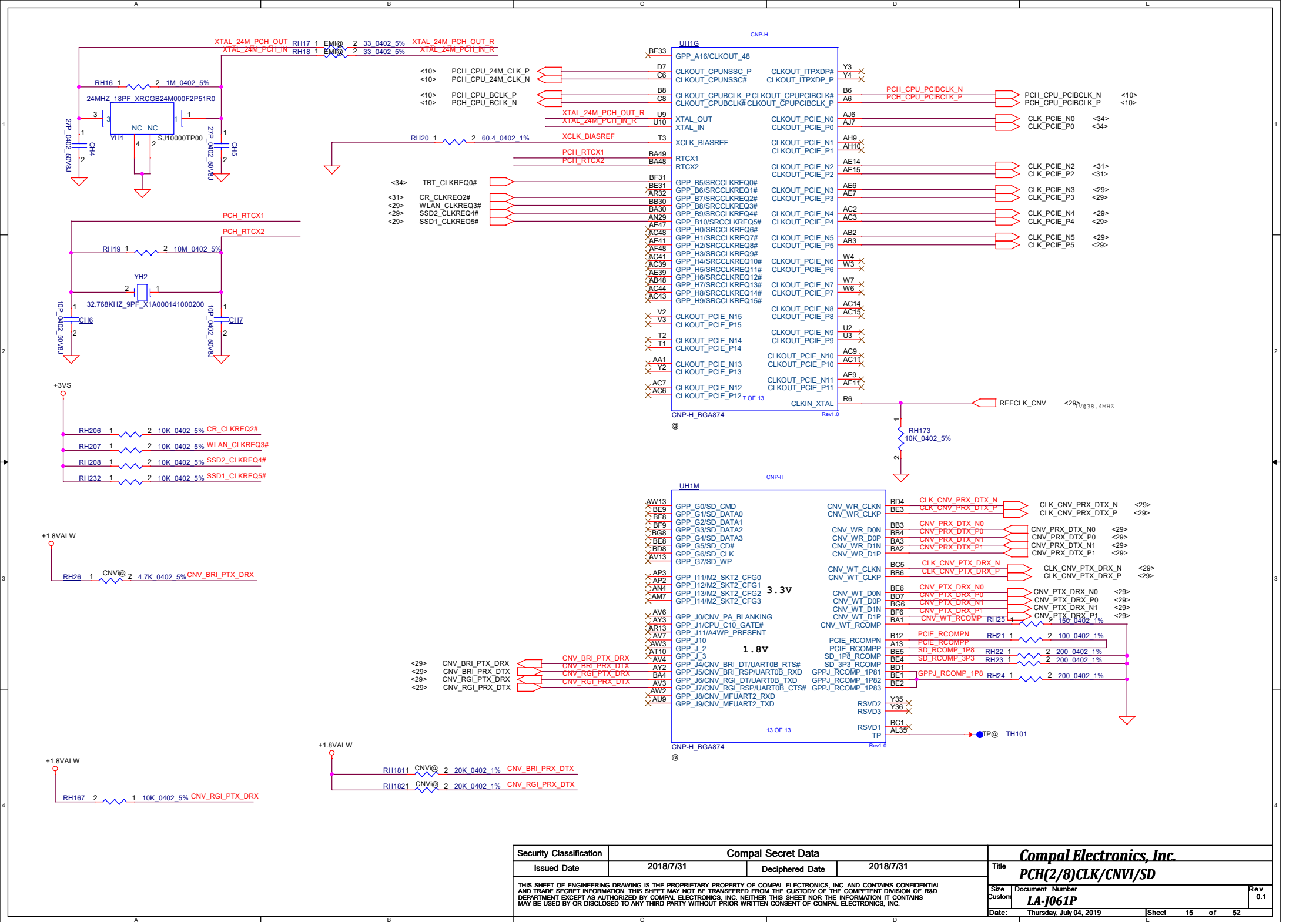


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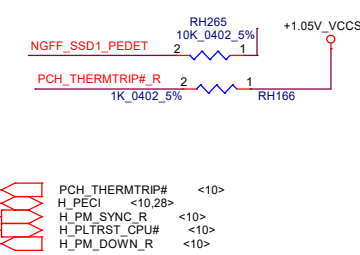
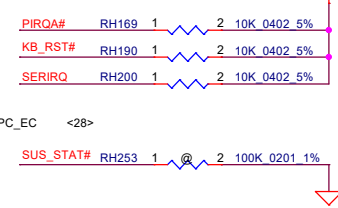




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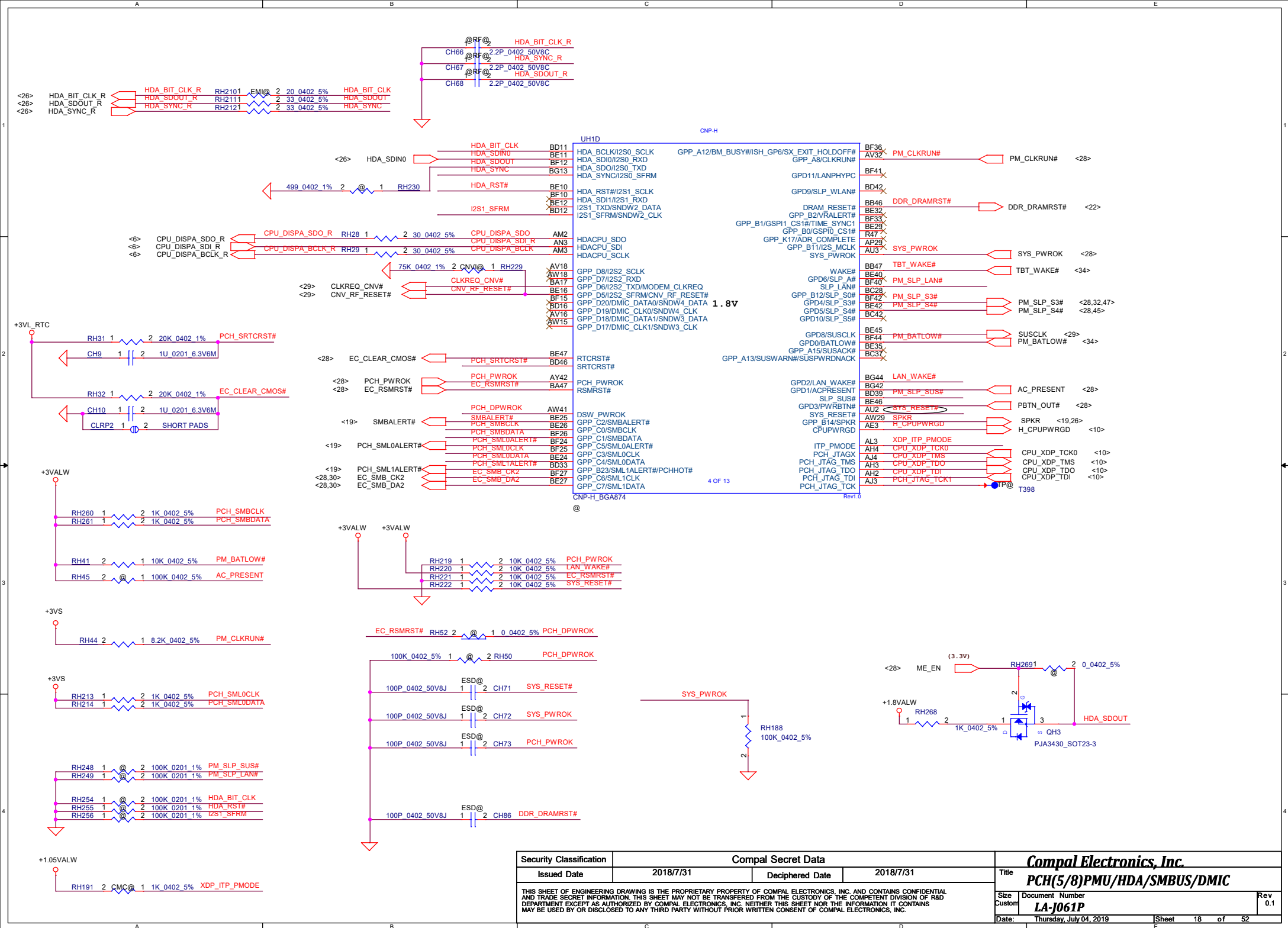


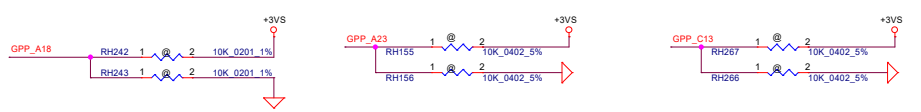
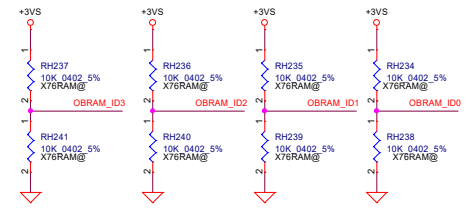
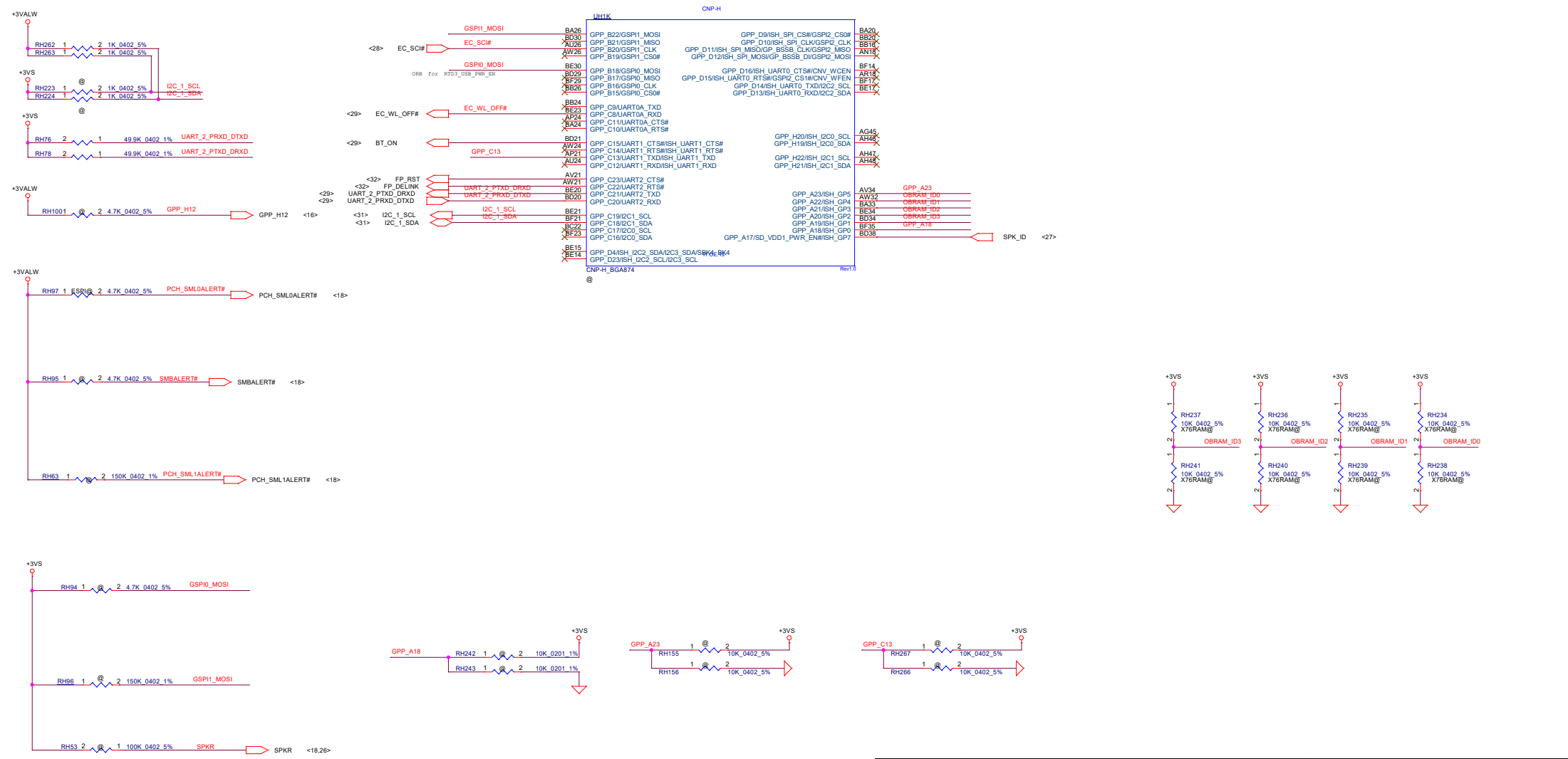




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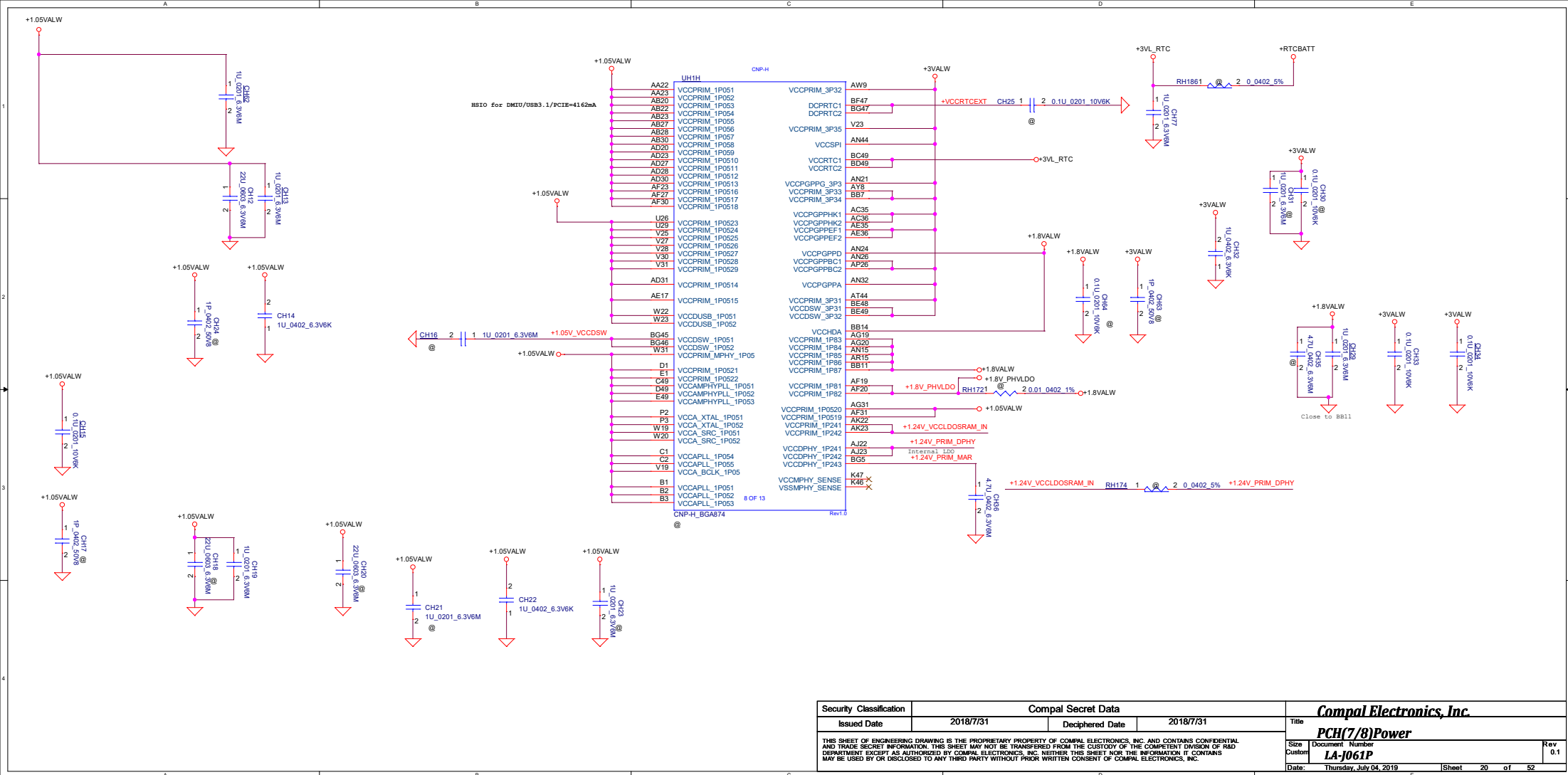


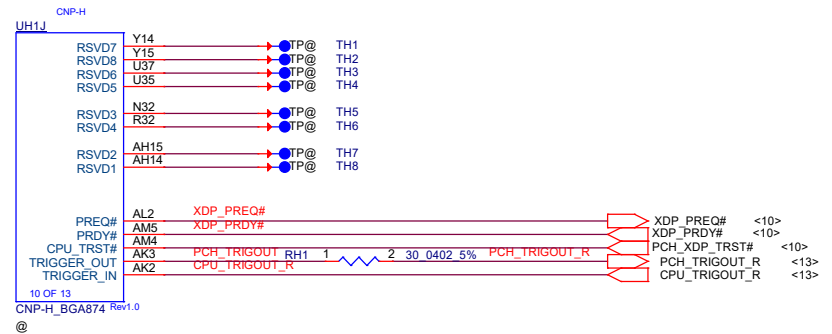
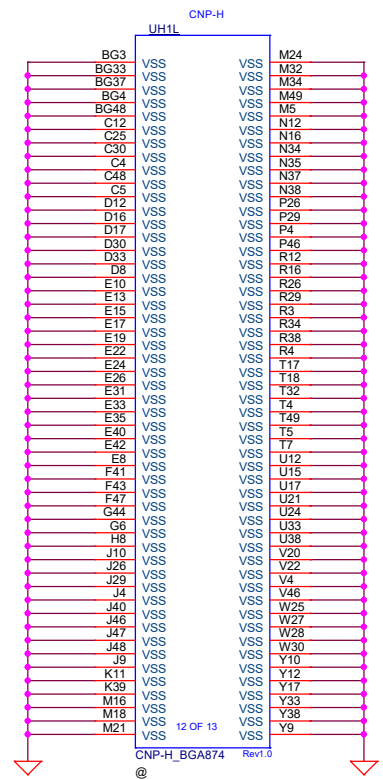
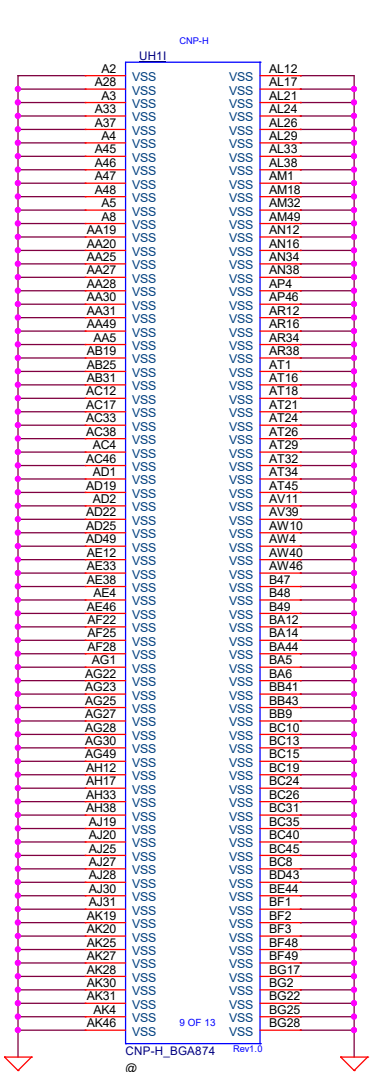




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Size		PCH(6/8)GPIO/I2C/UART/STRAP	
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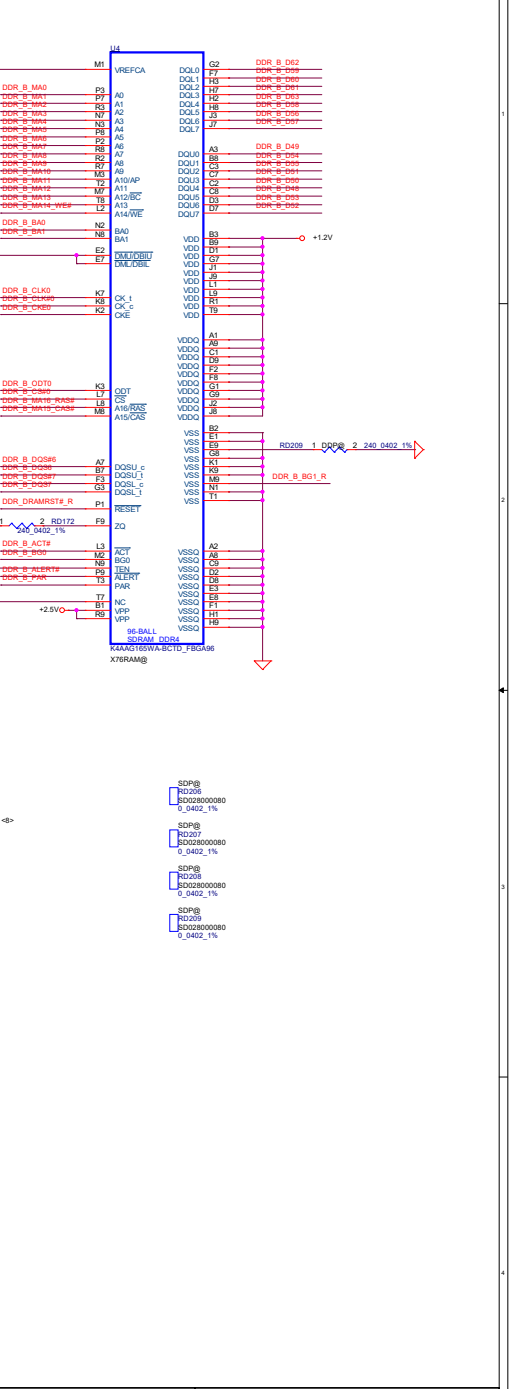
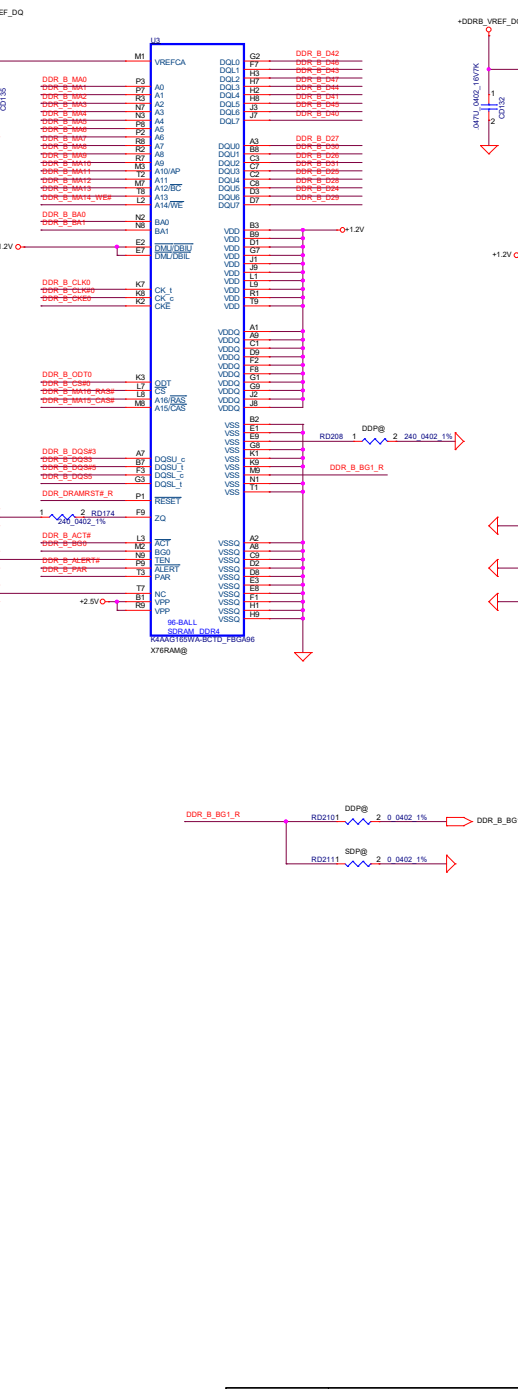
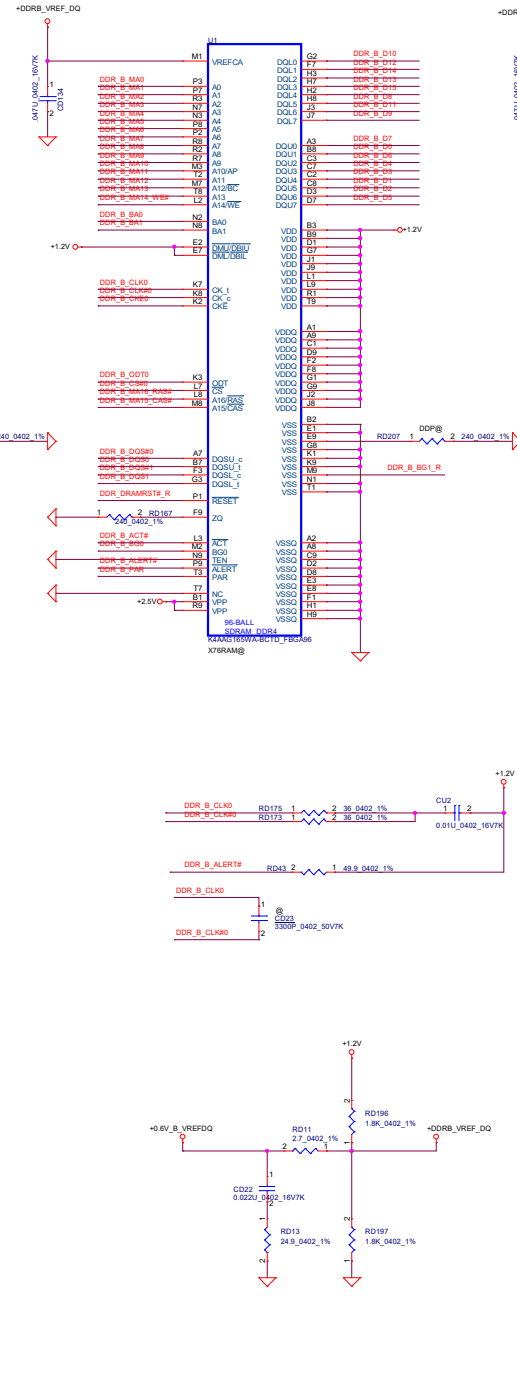
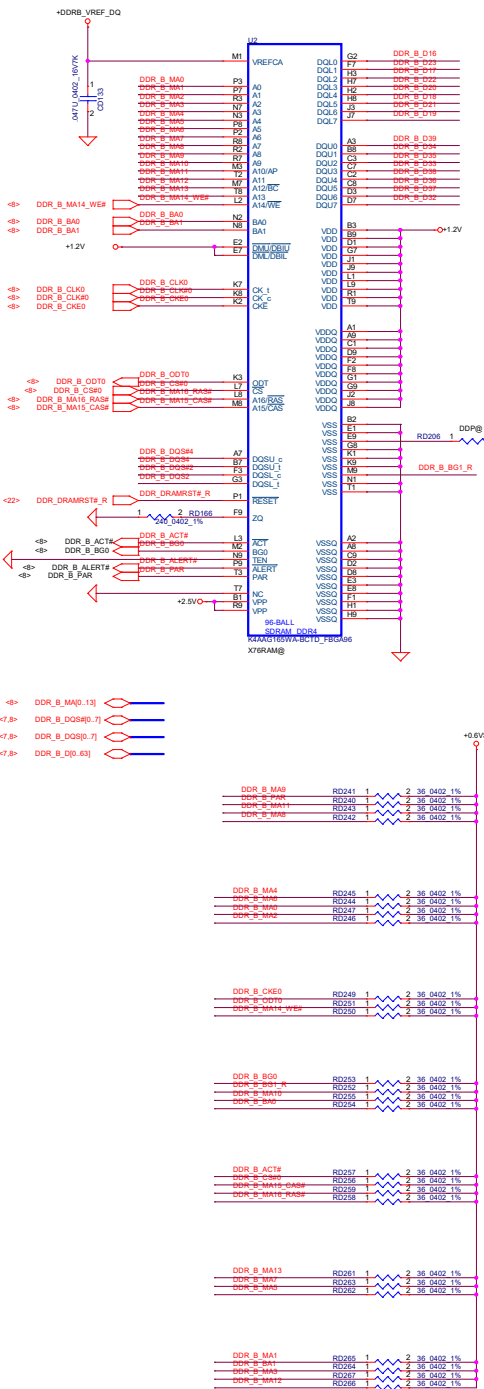


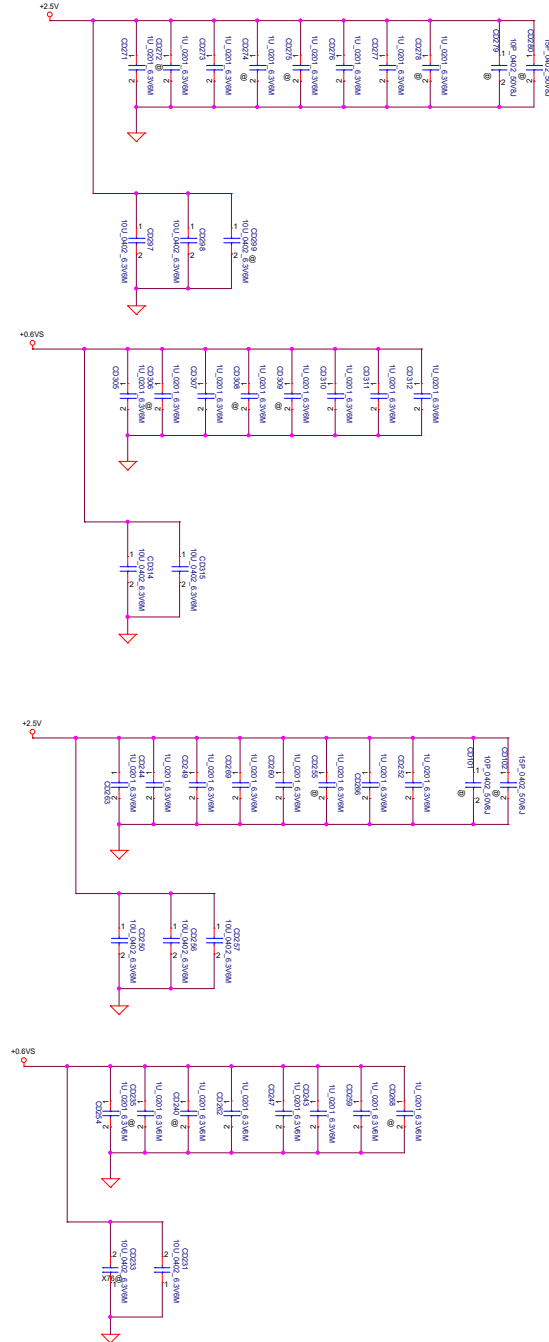
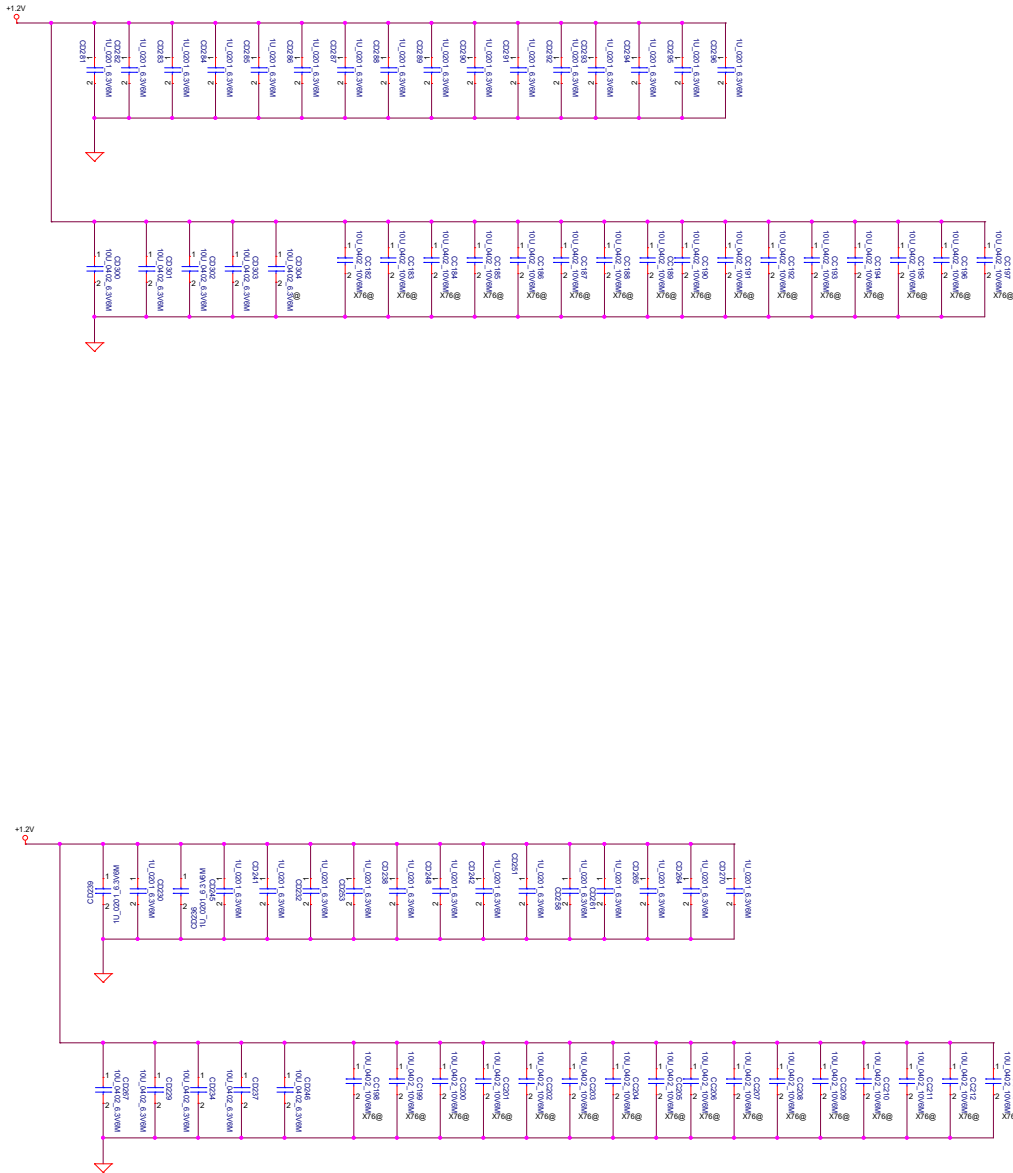


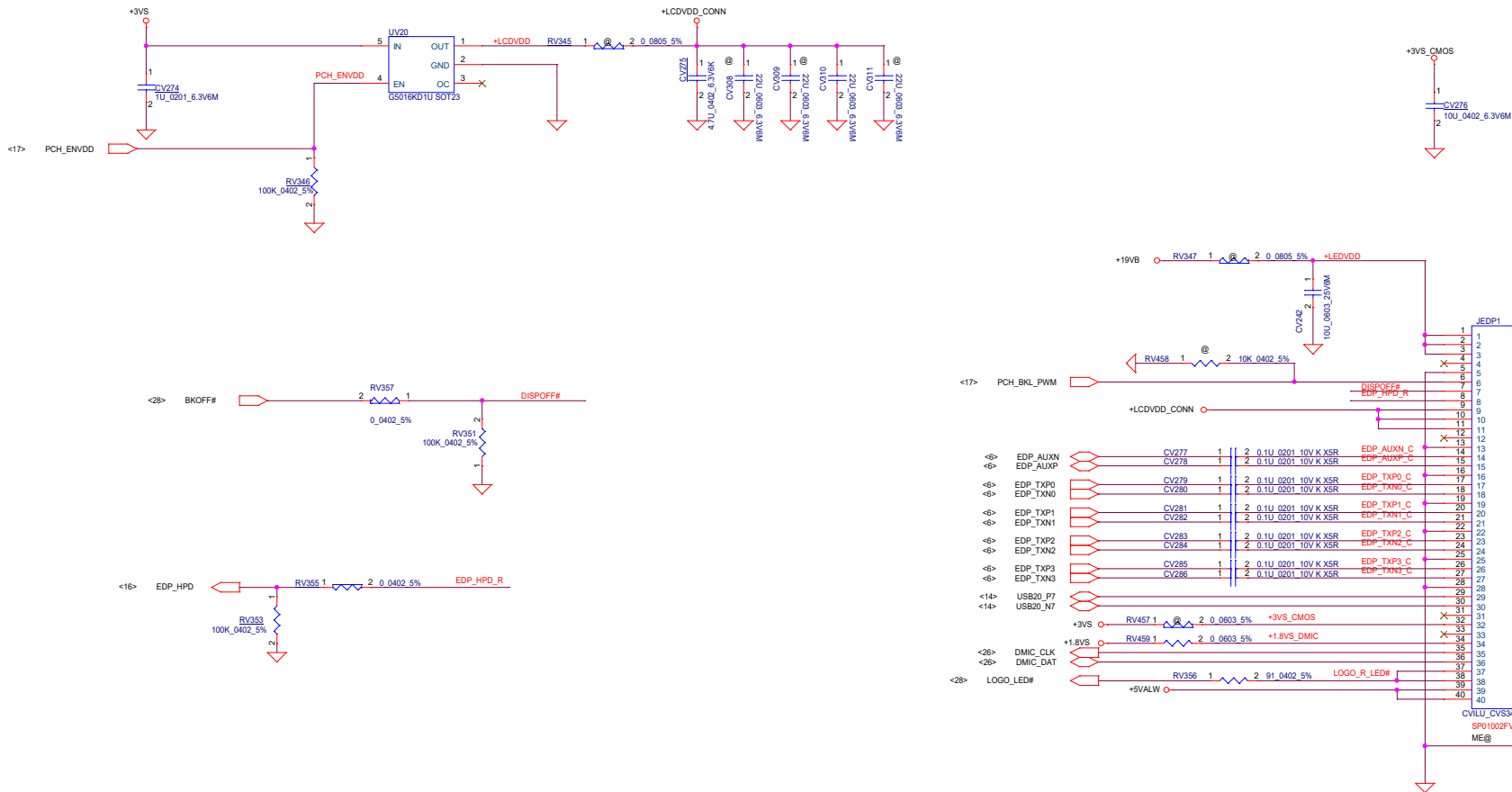


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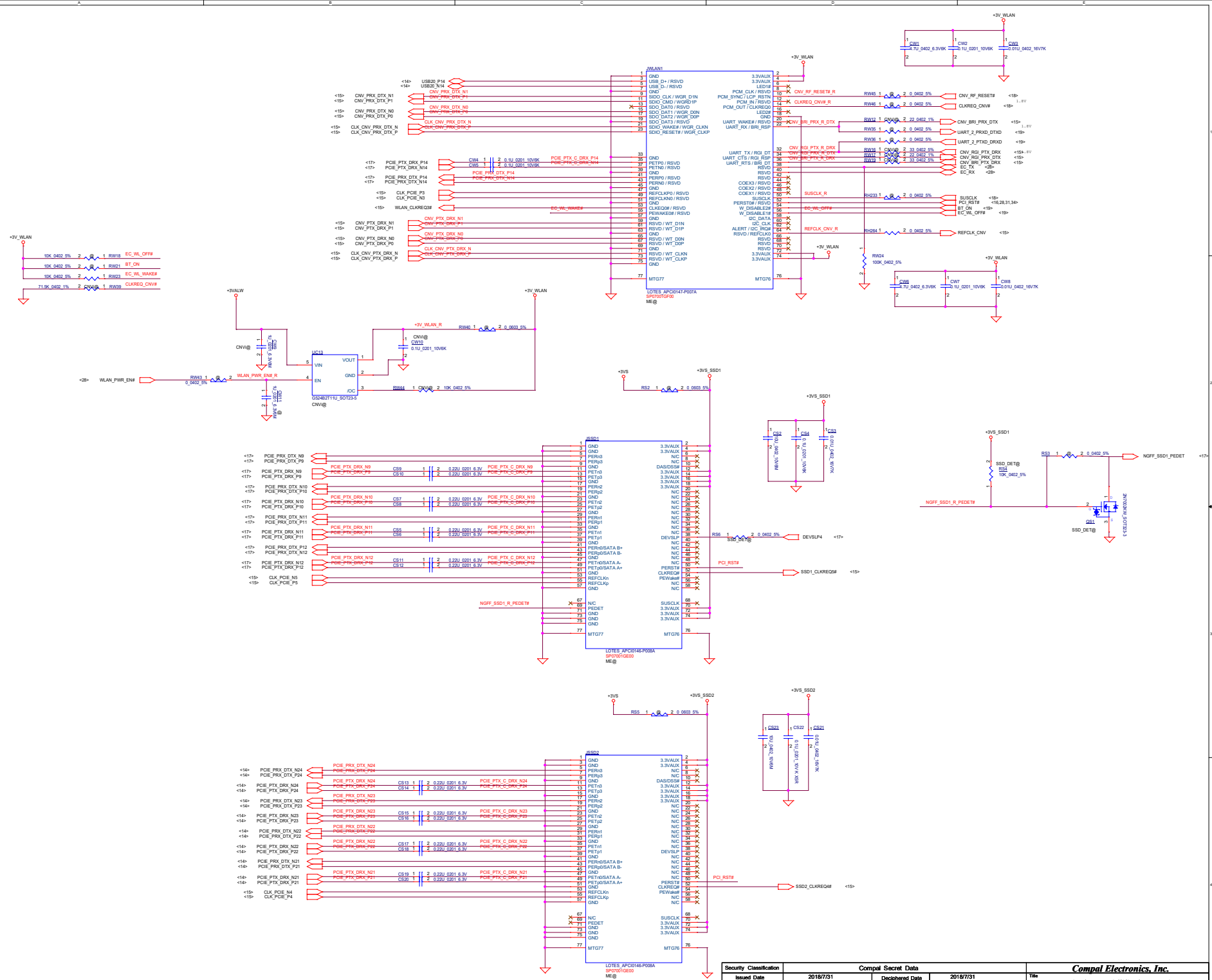




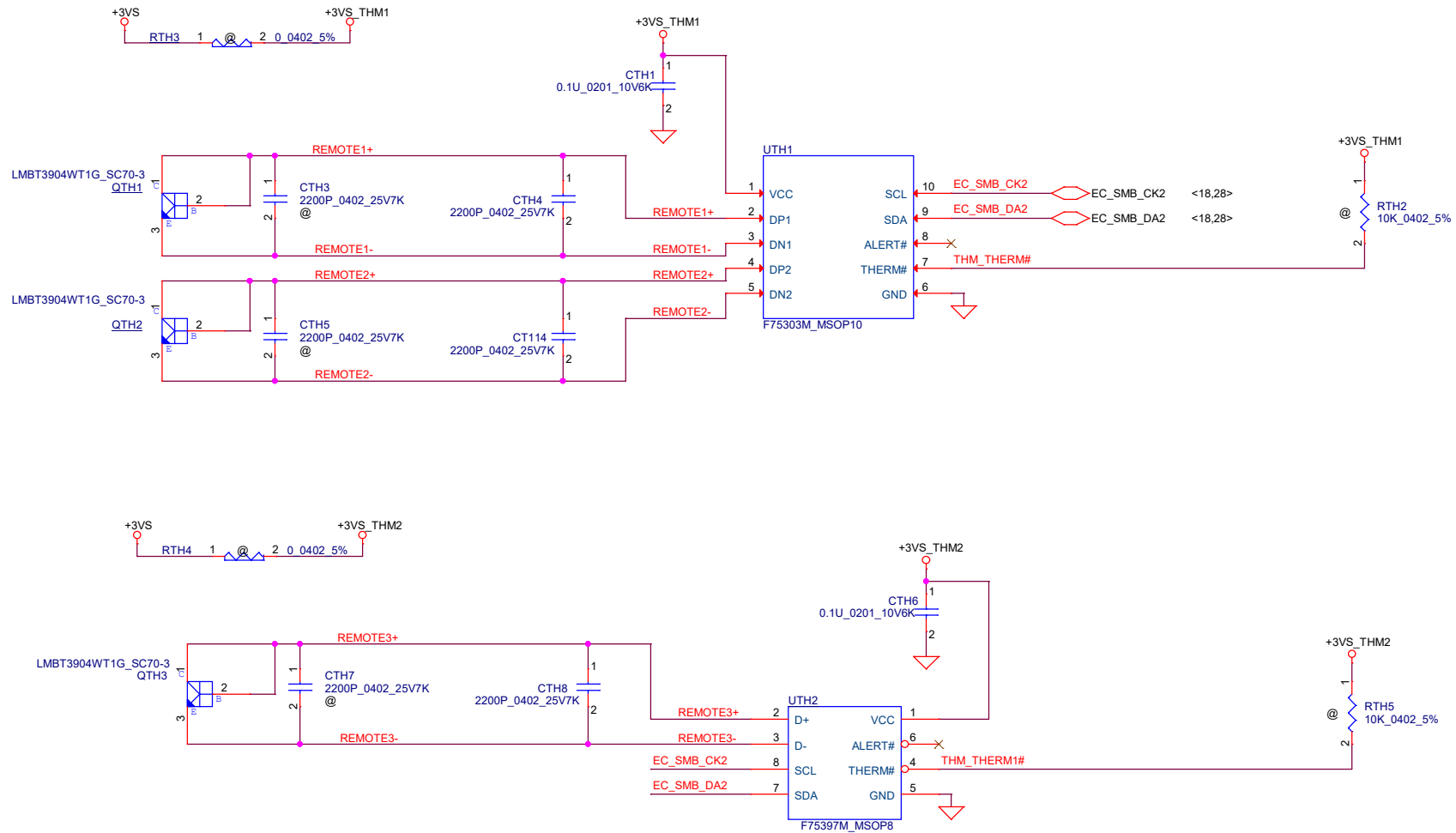




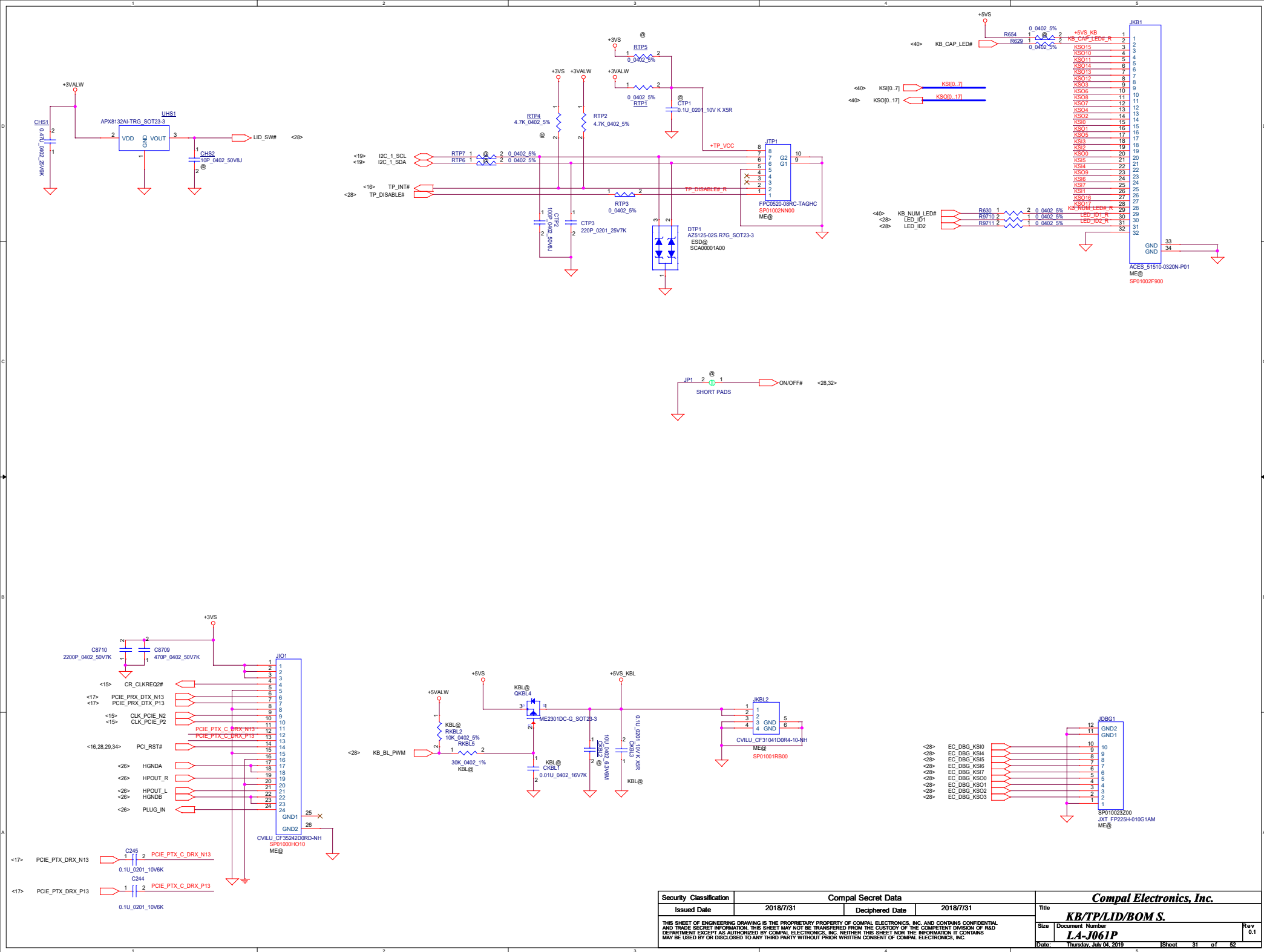


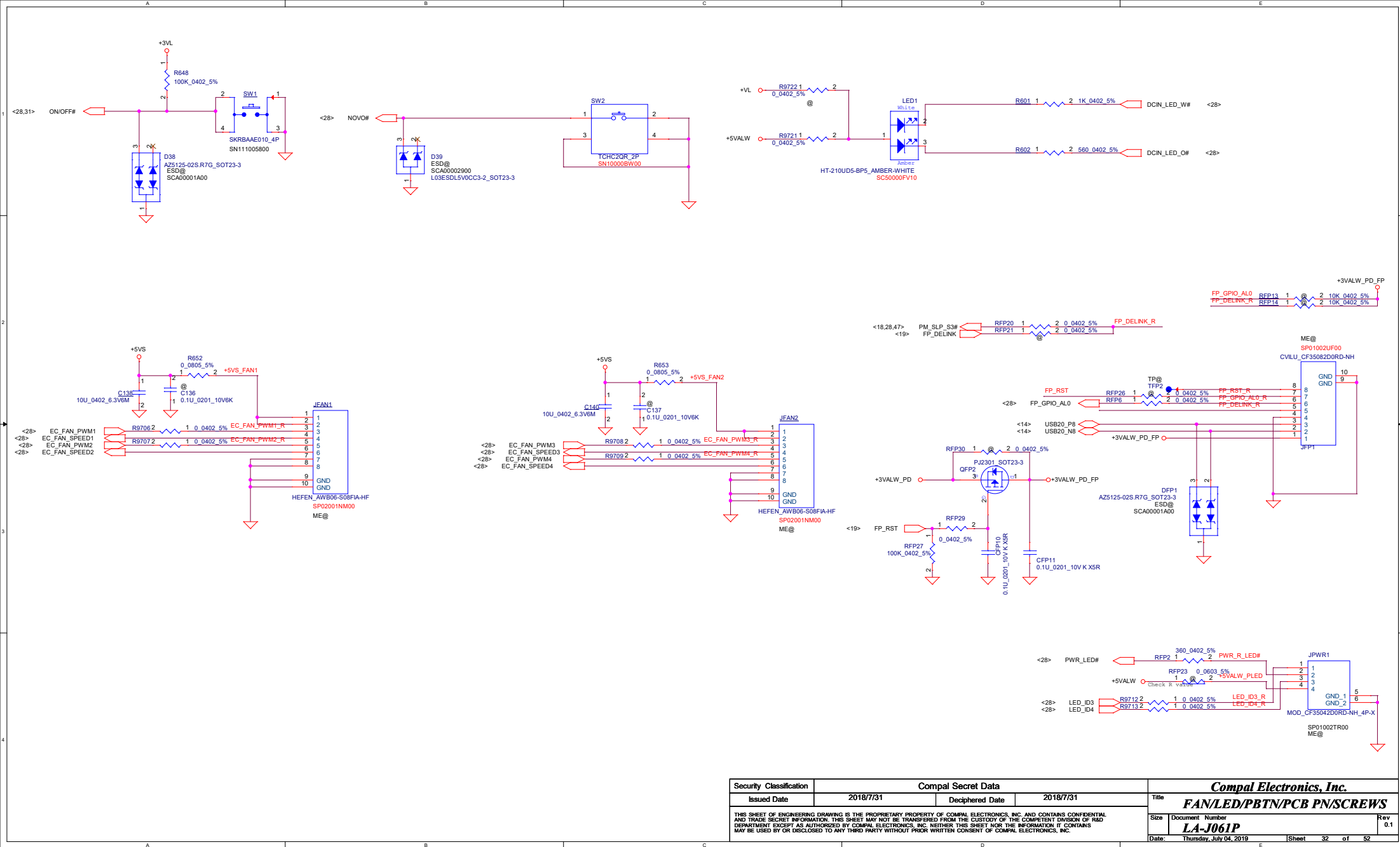


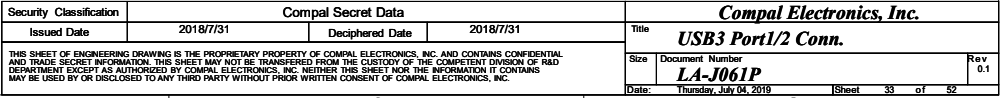
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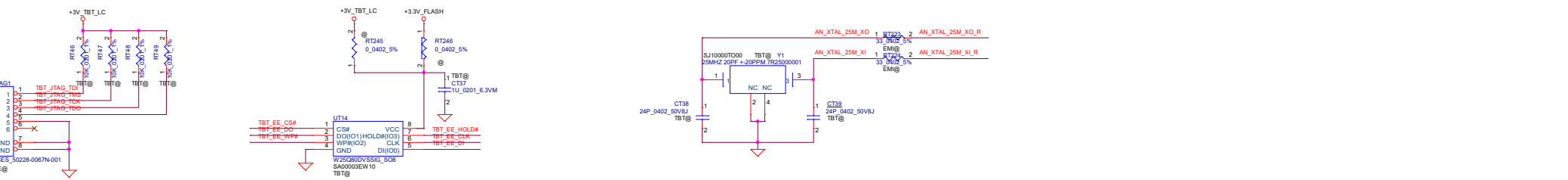
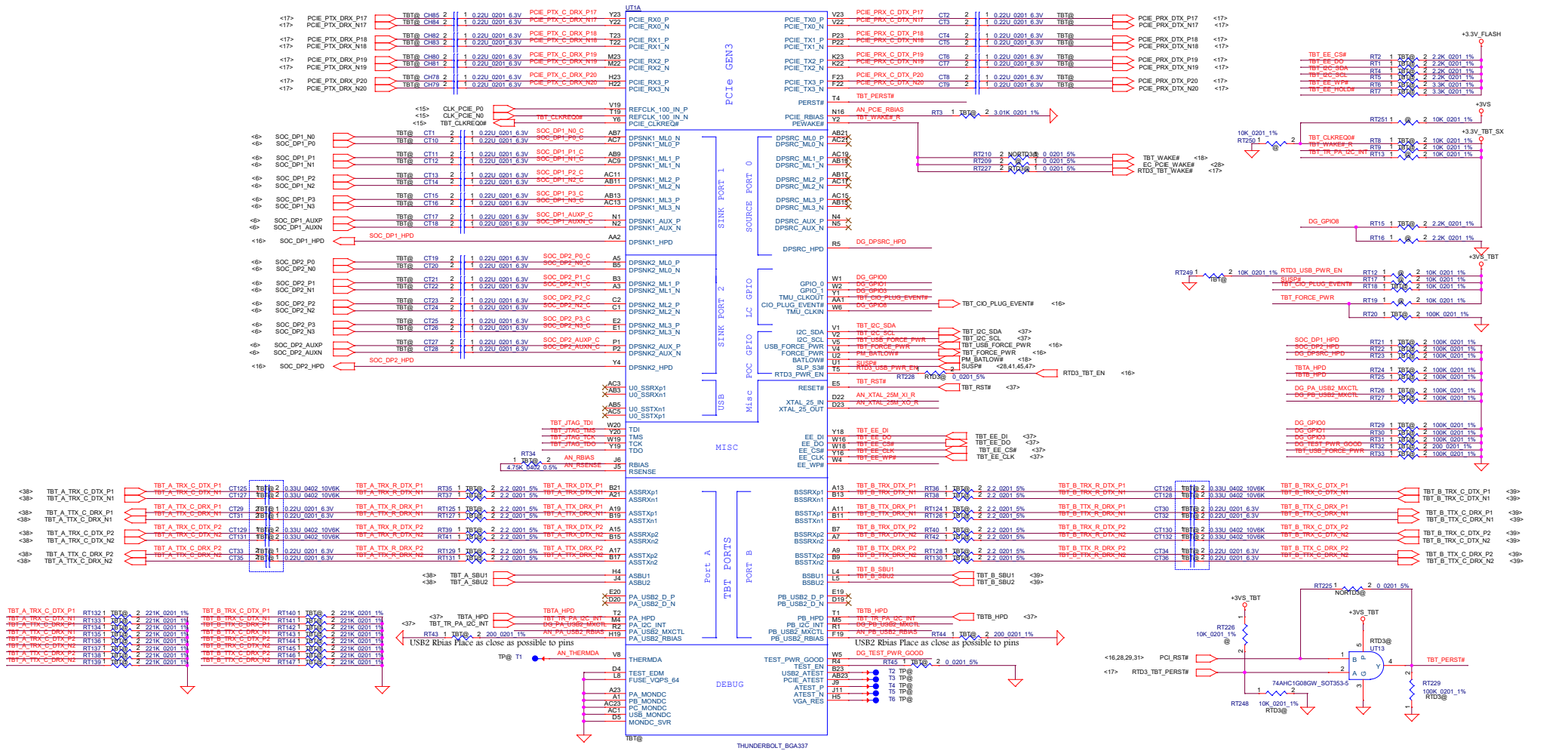


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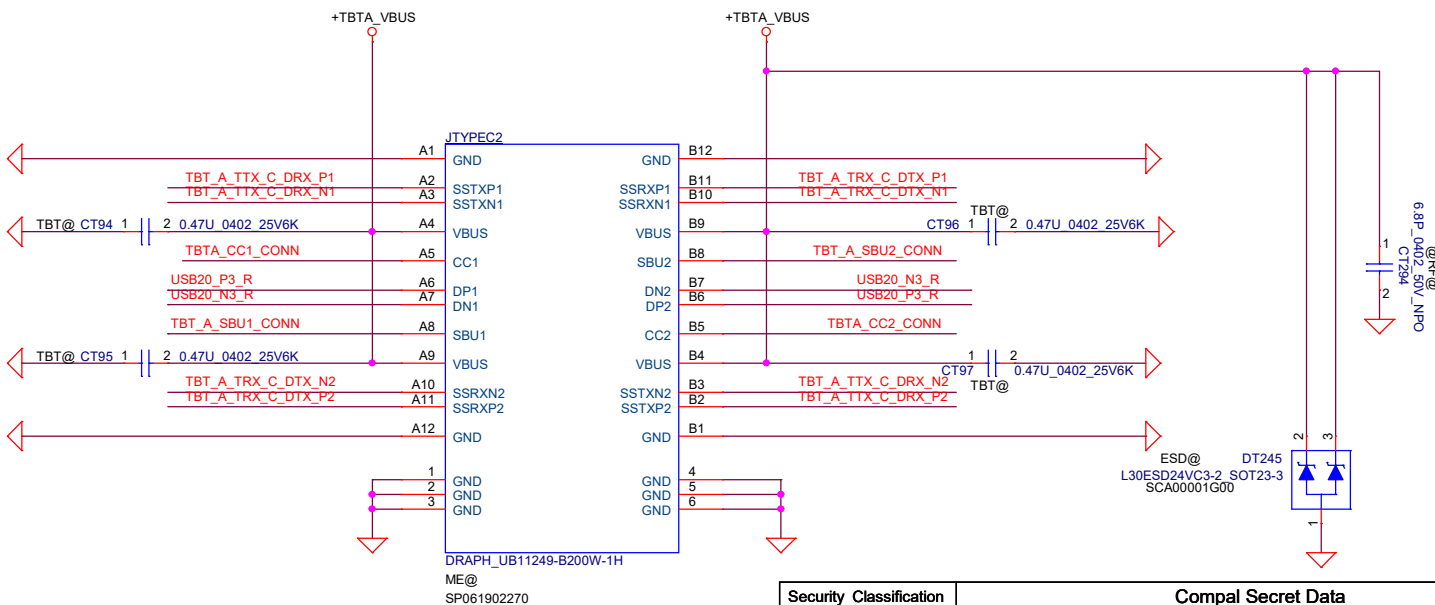
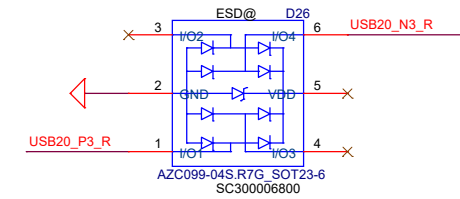
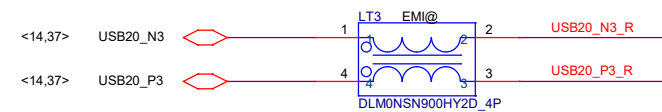
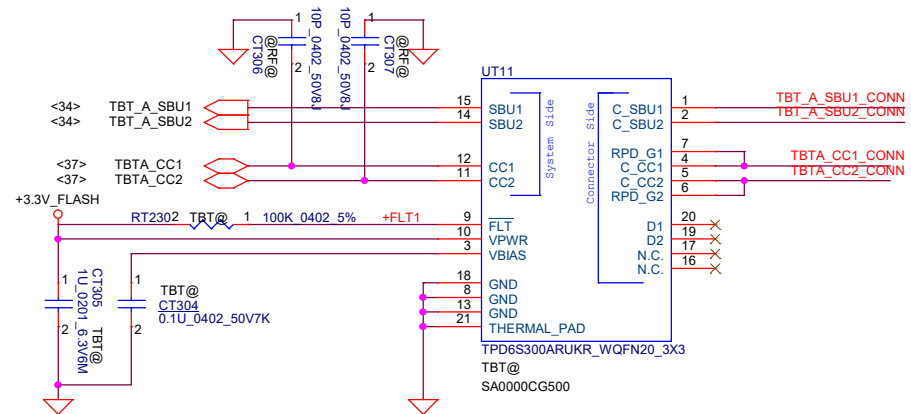
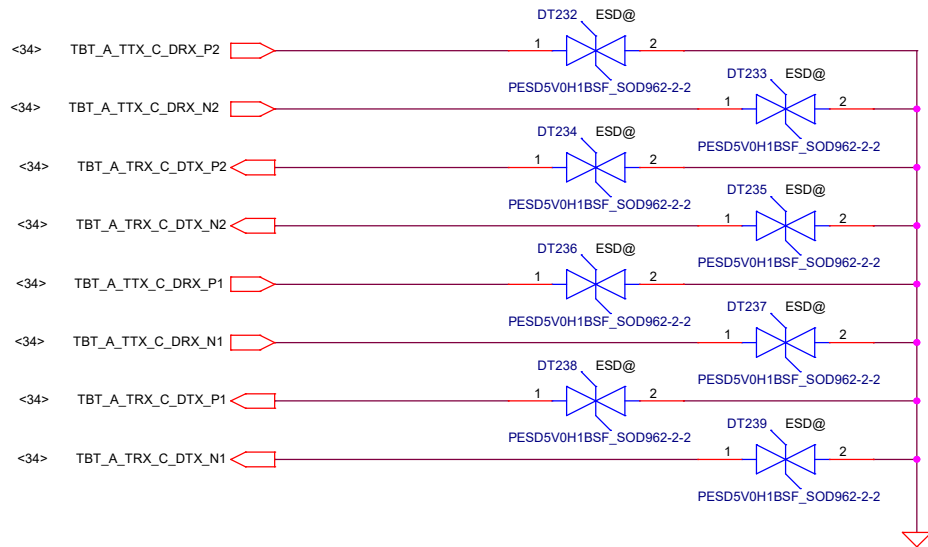




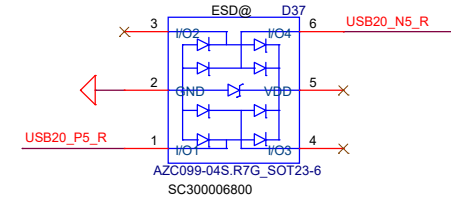
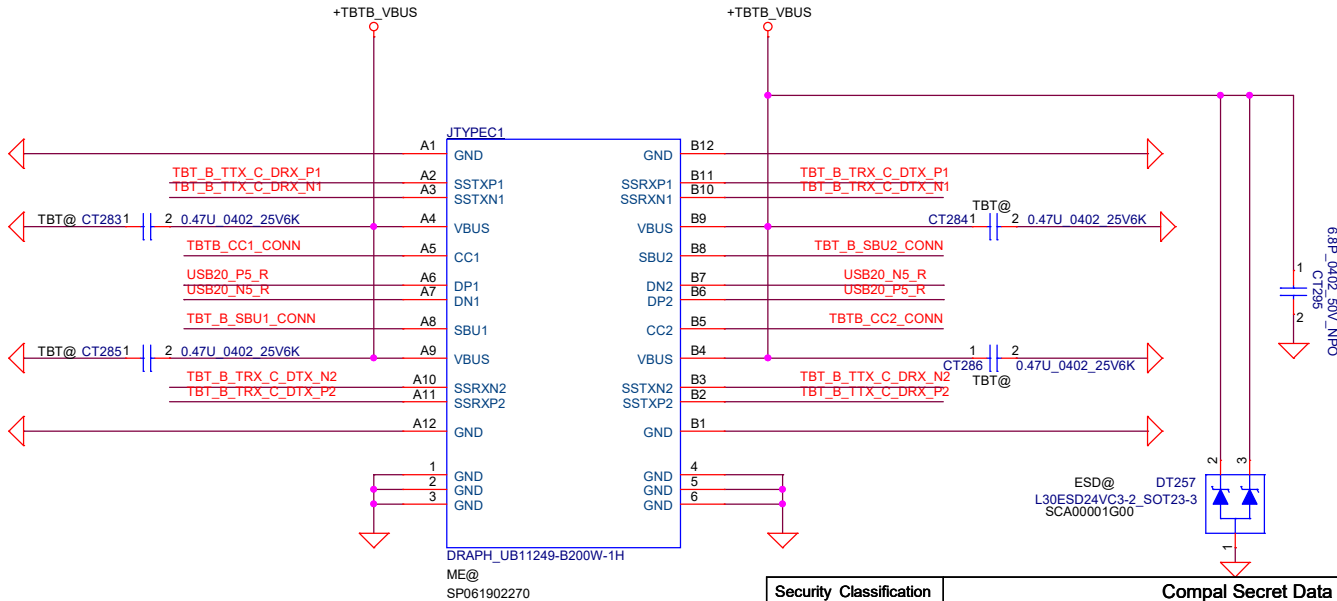
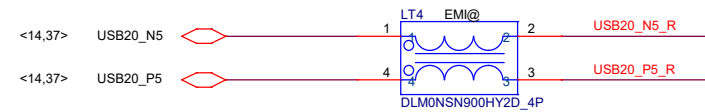
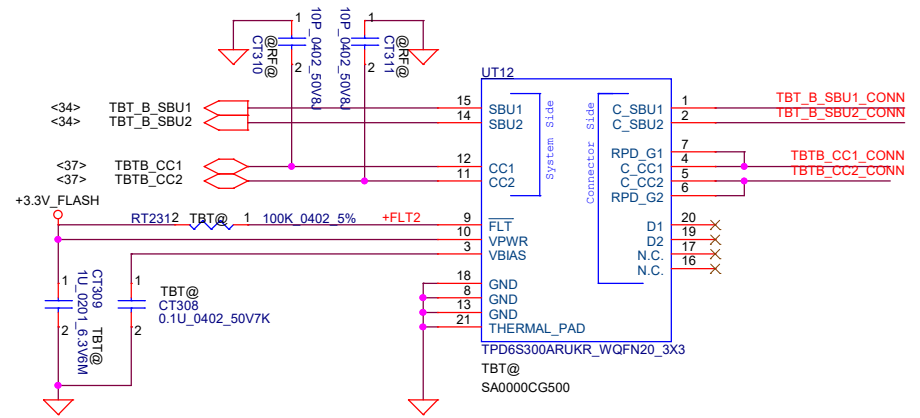
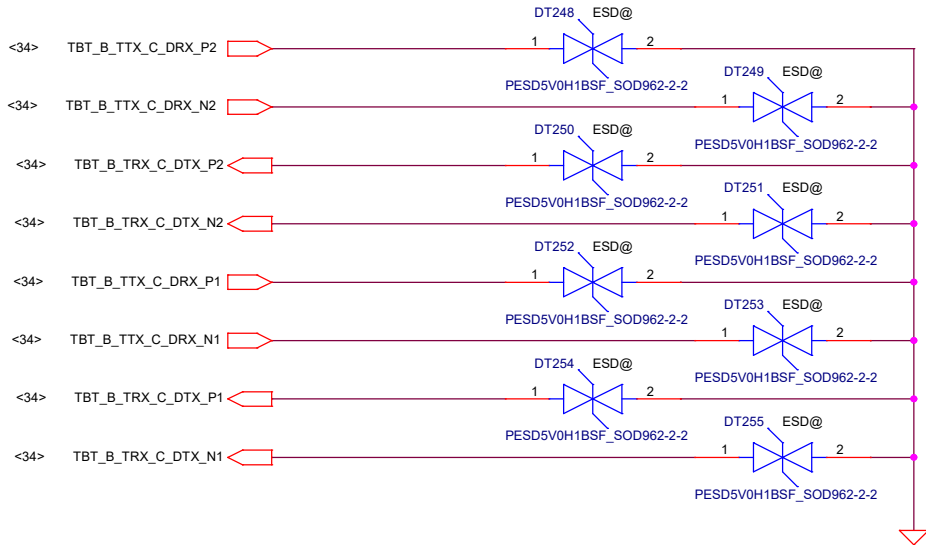




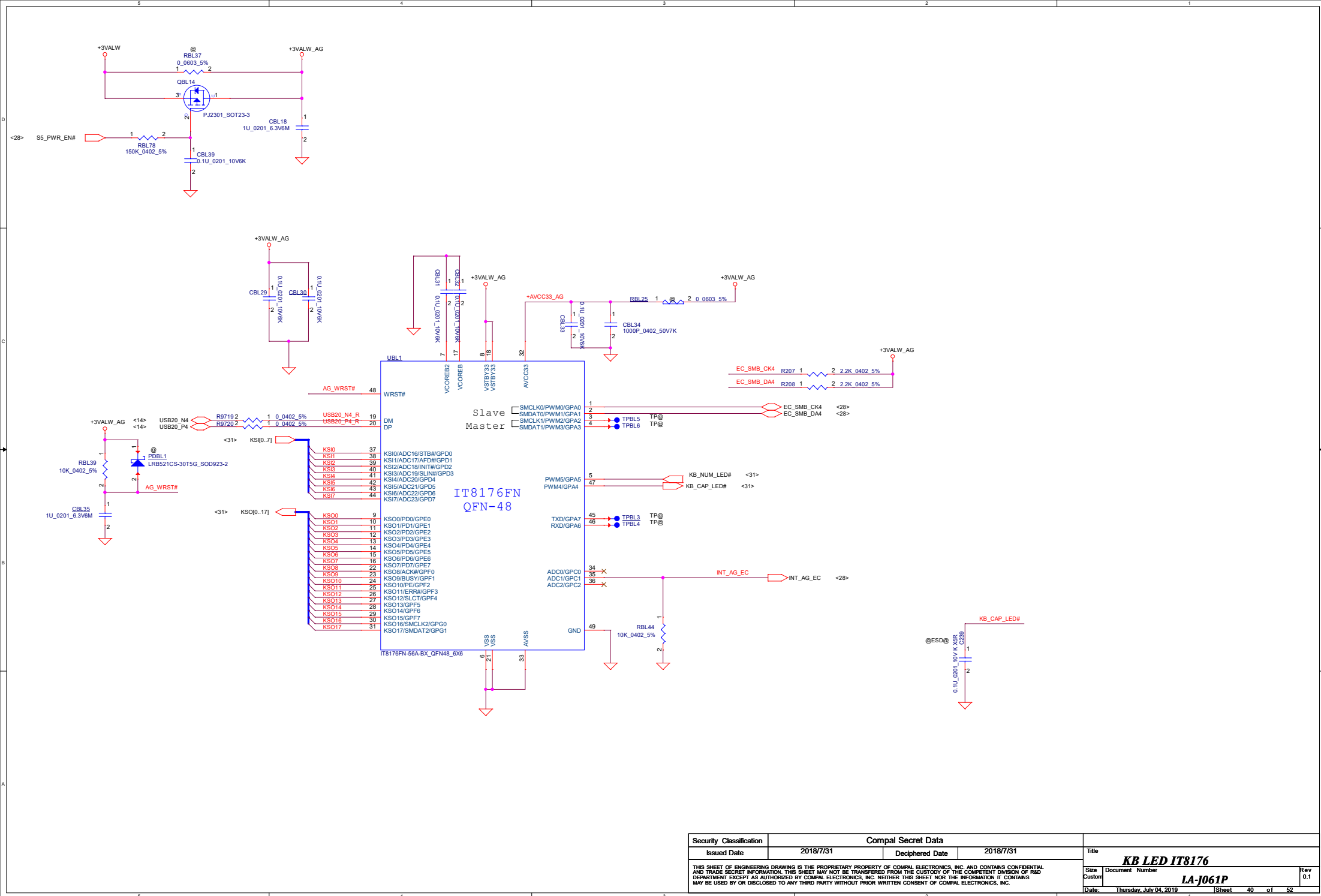


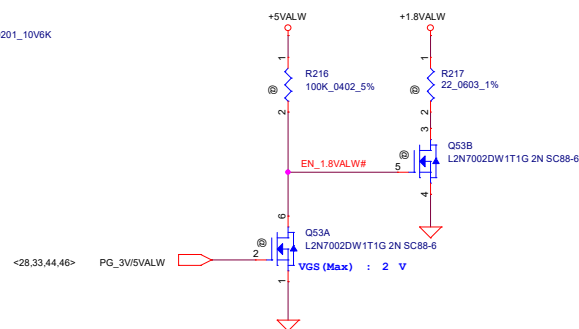
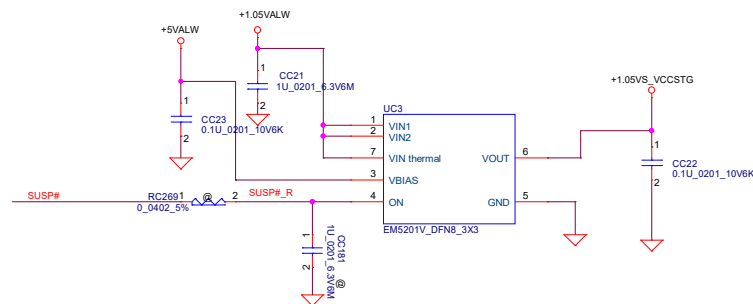
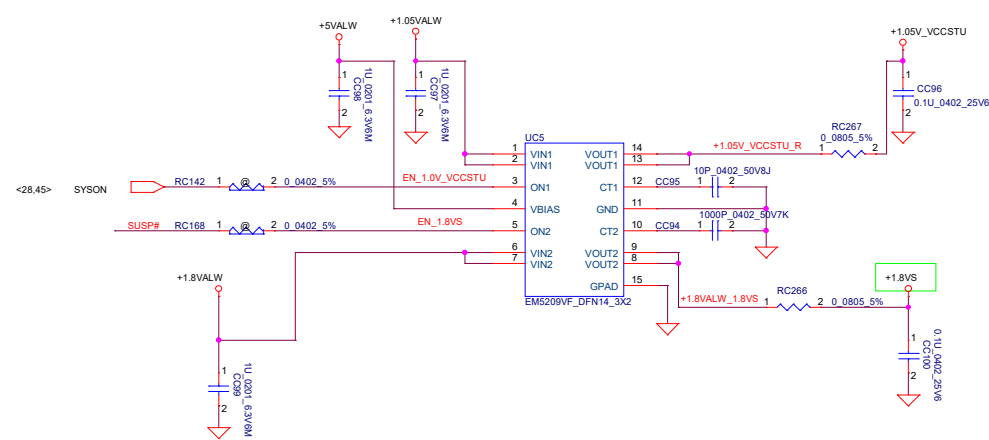
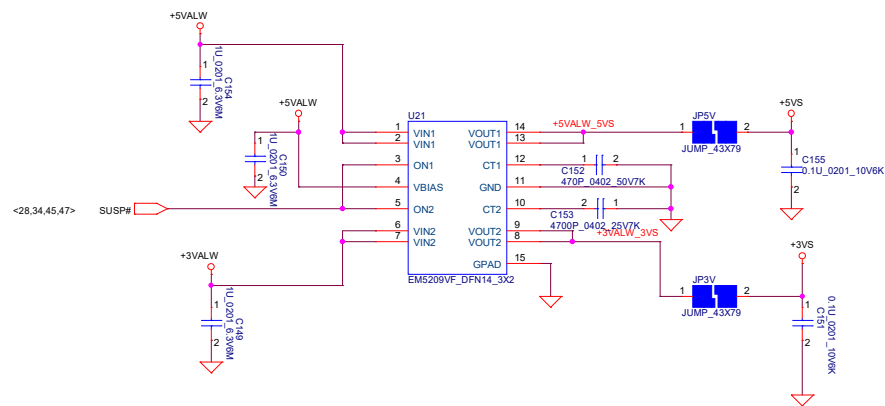


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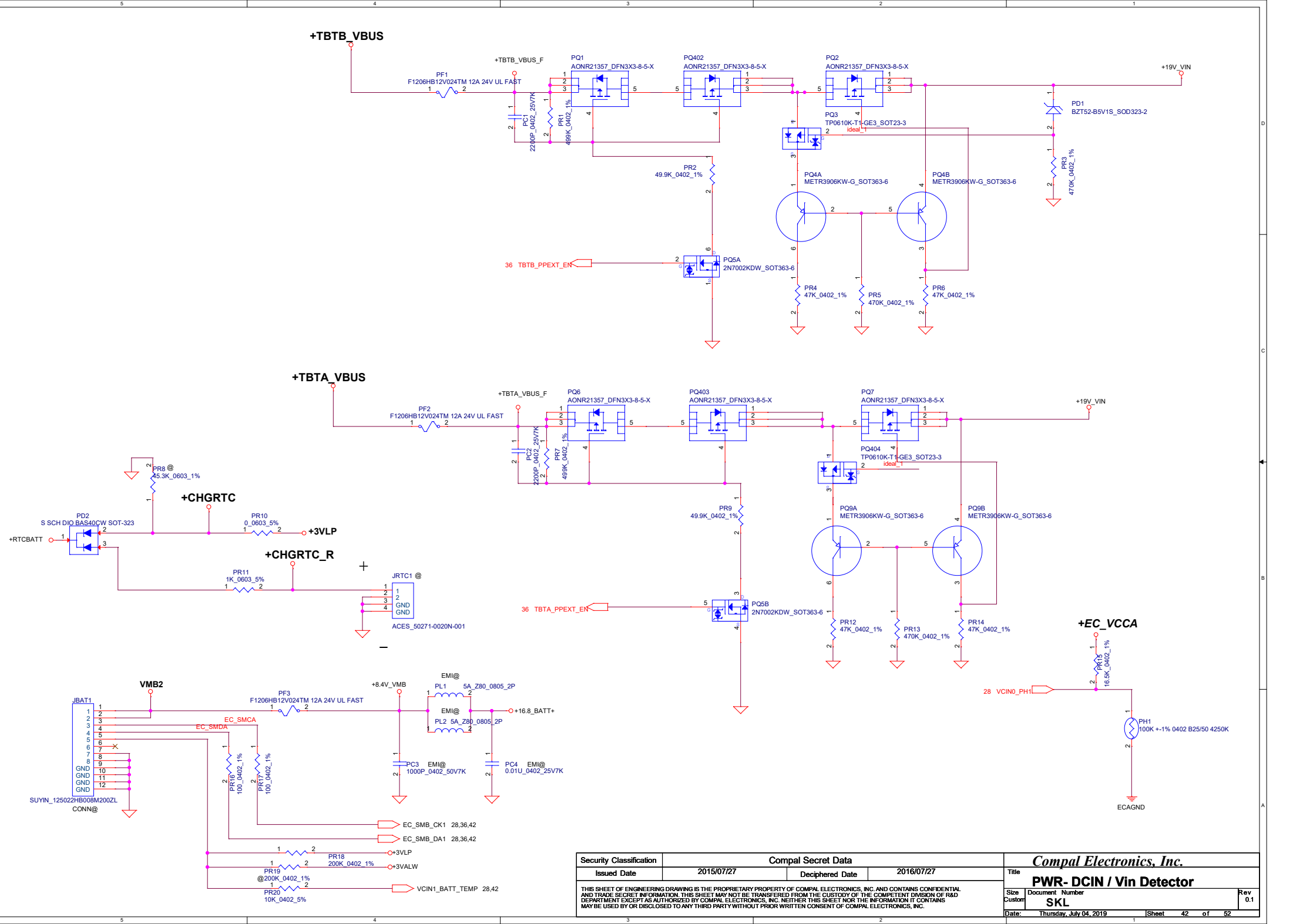


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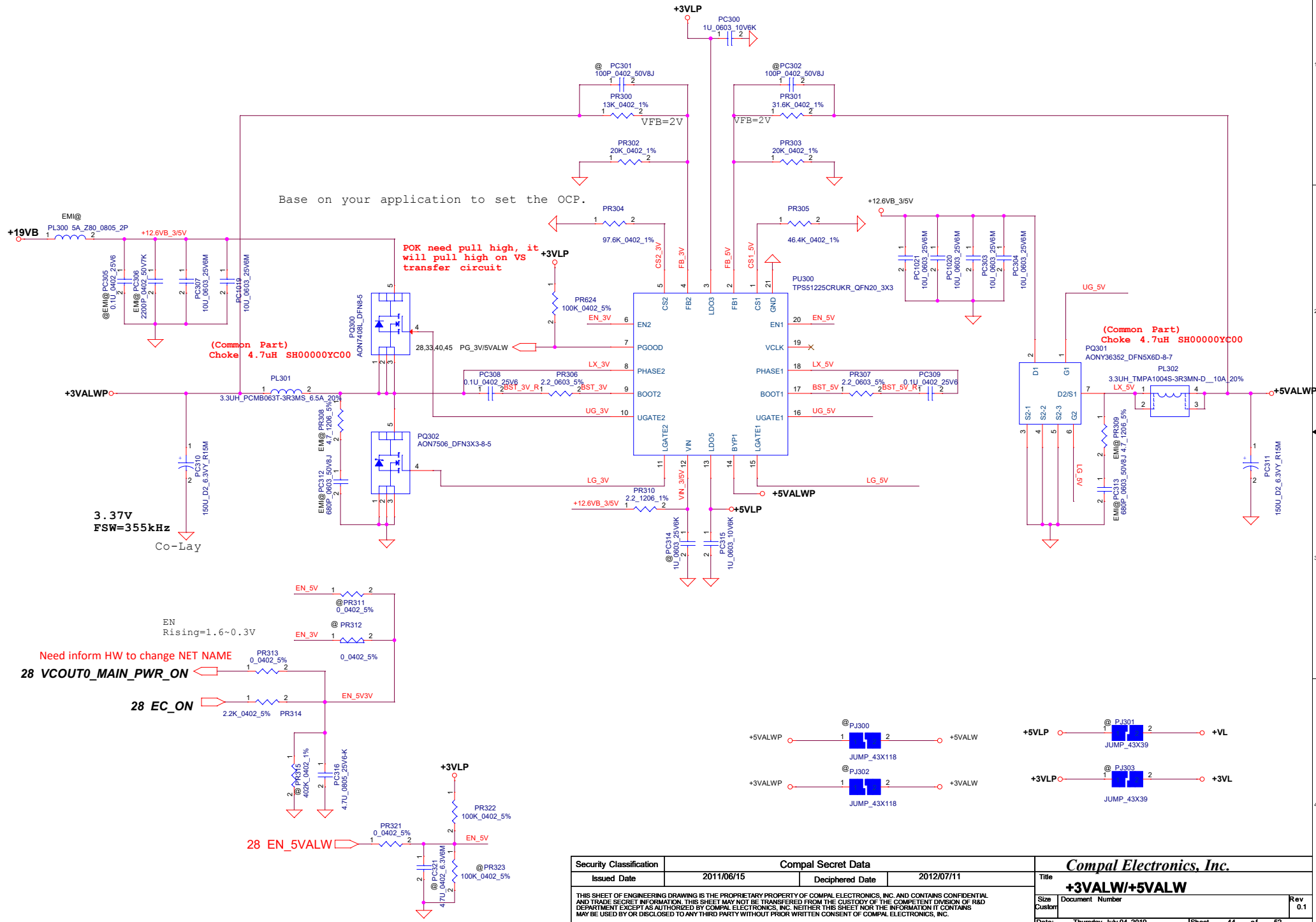


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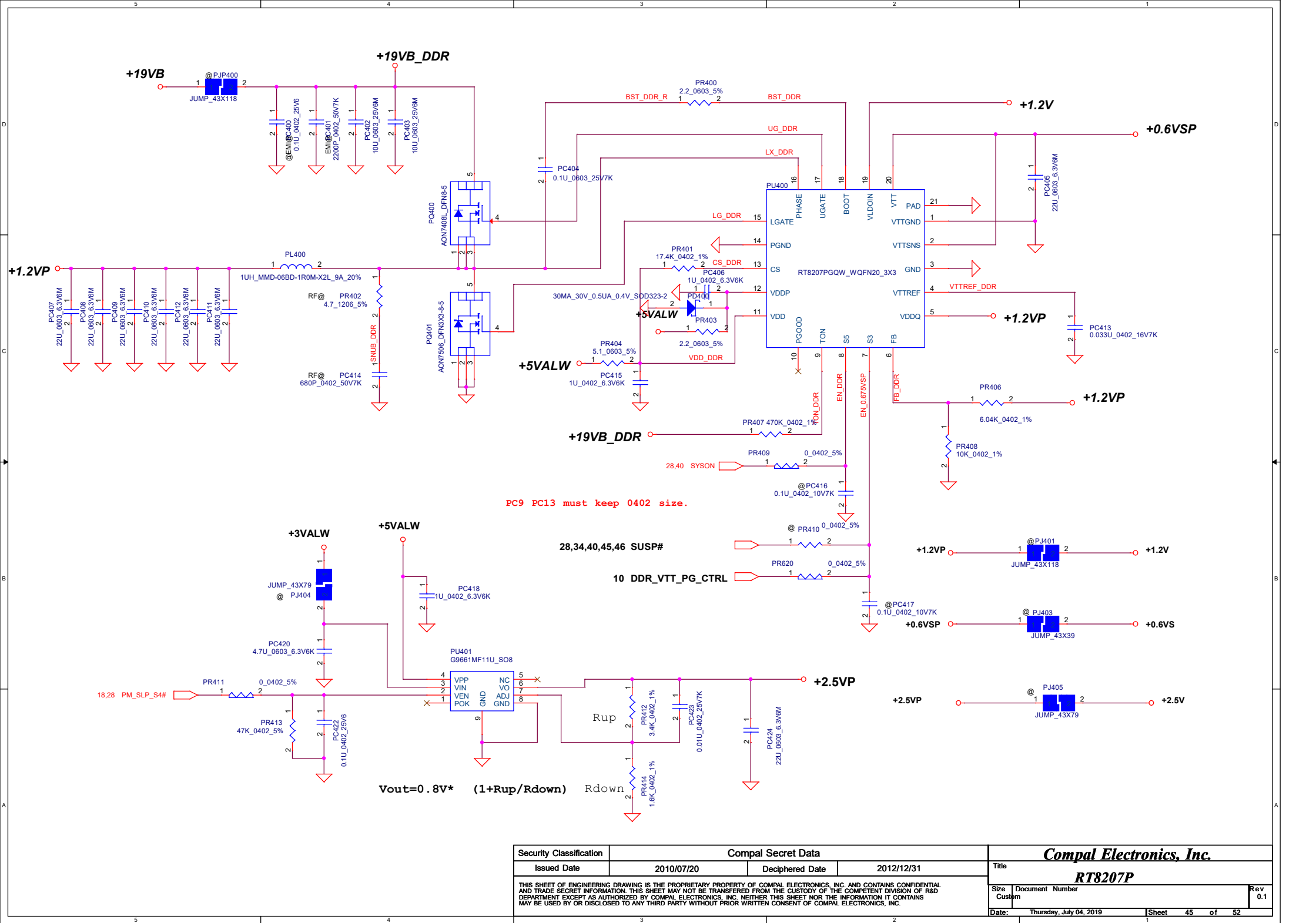


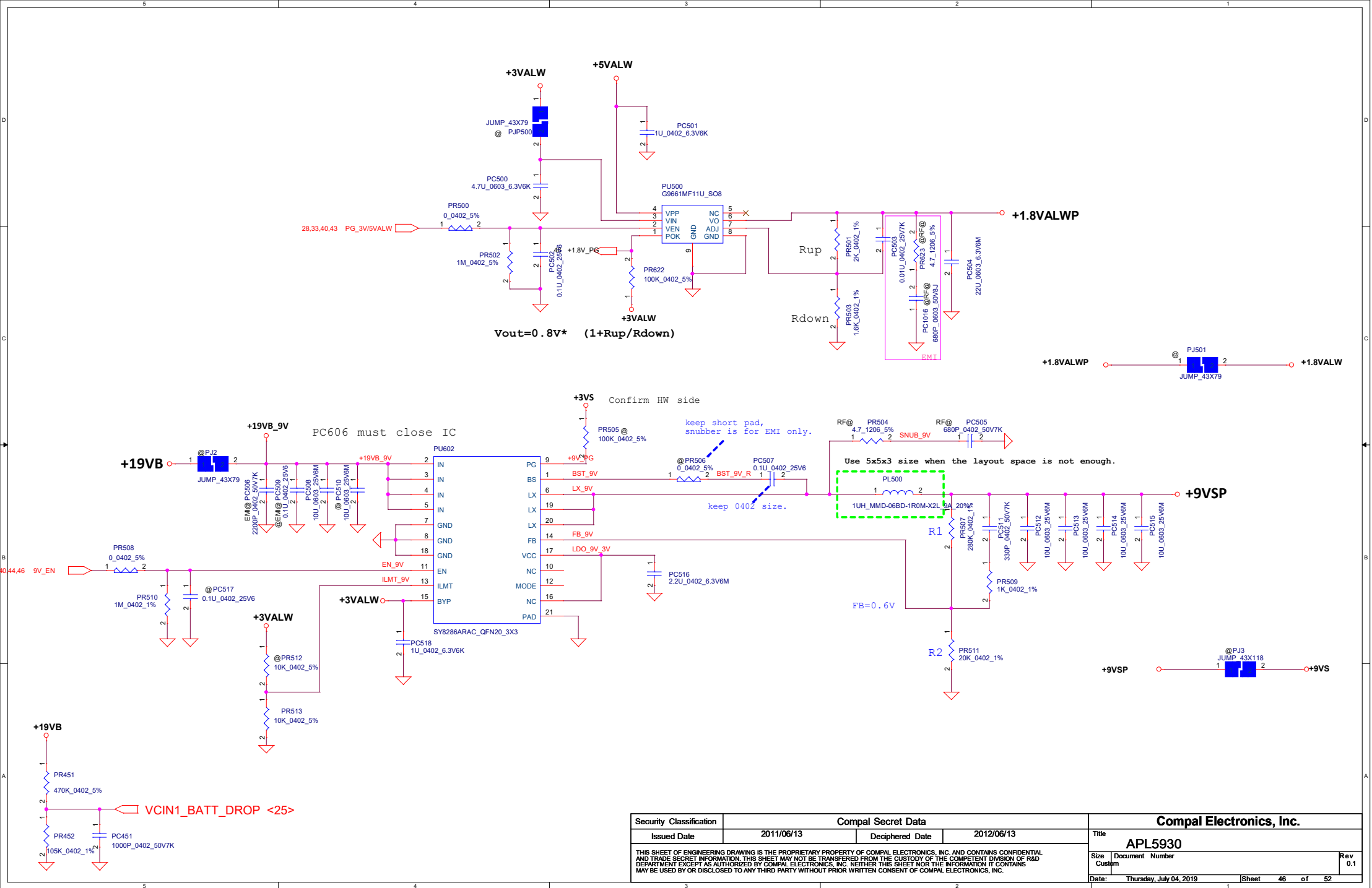
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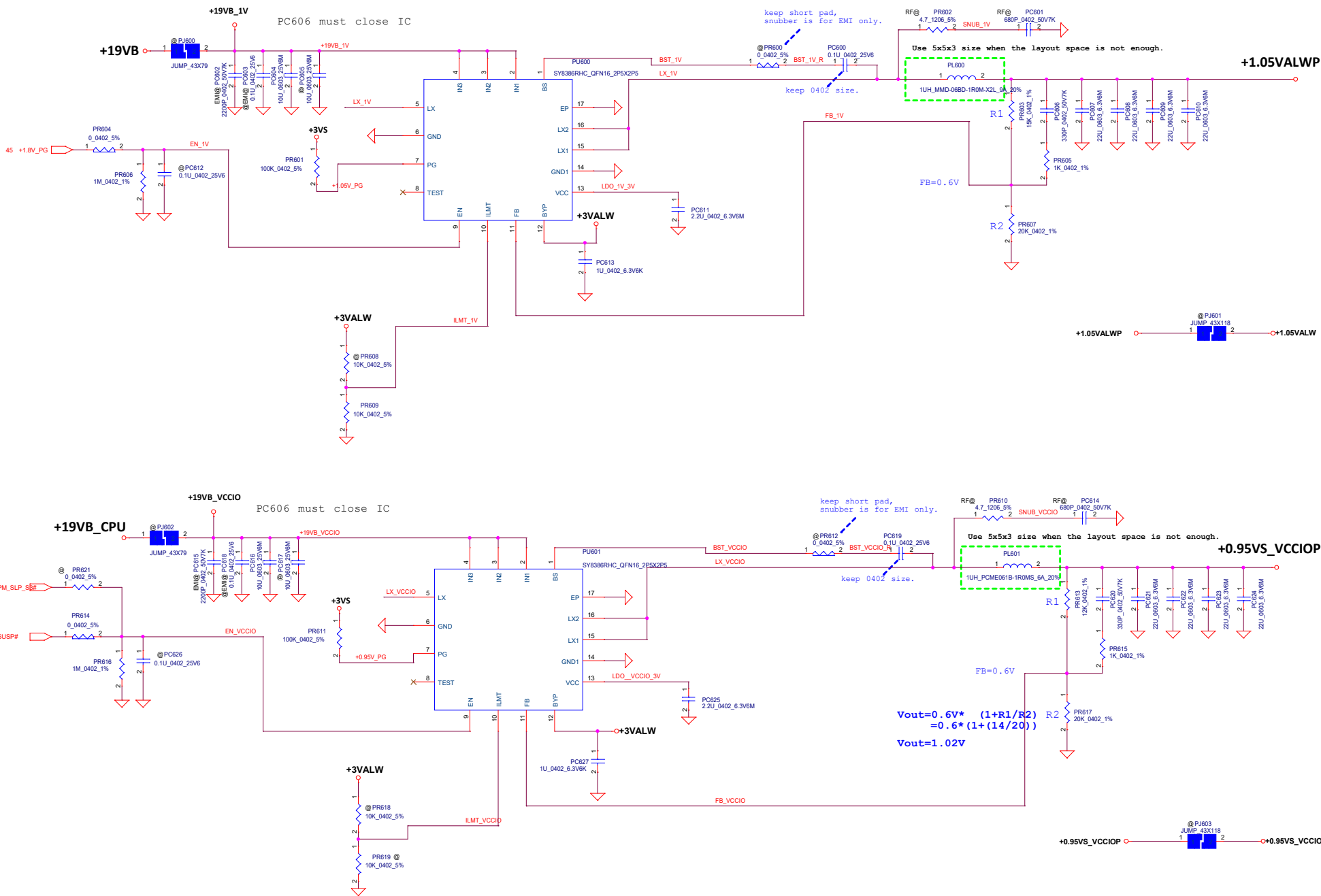




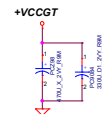
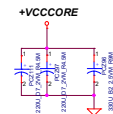
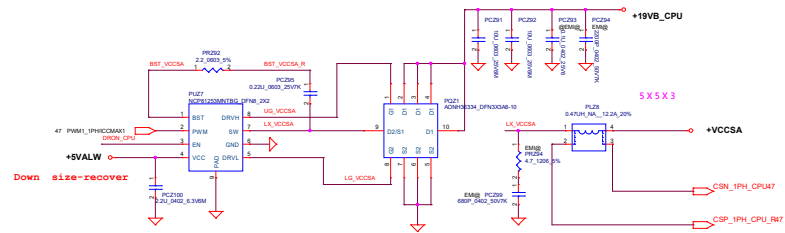
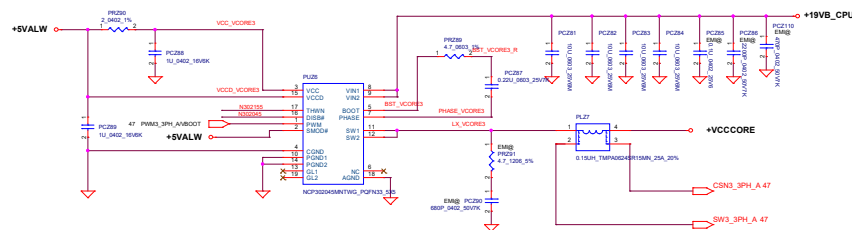
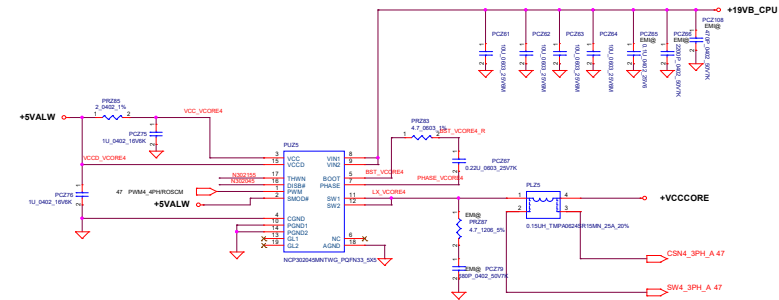
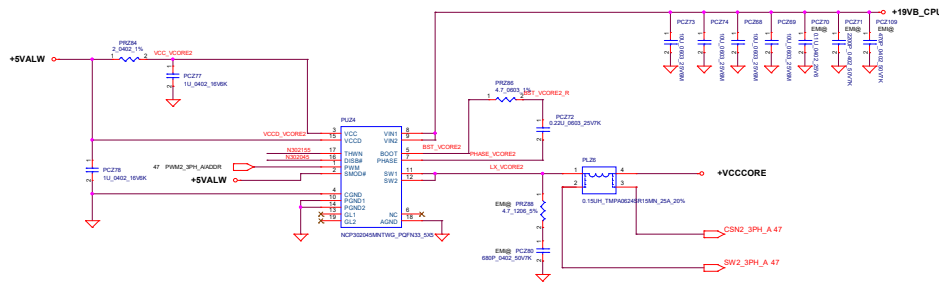
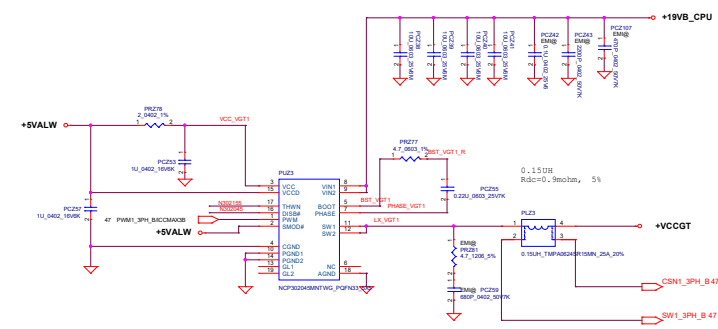
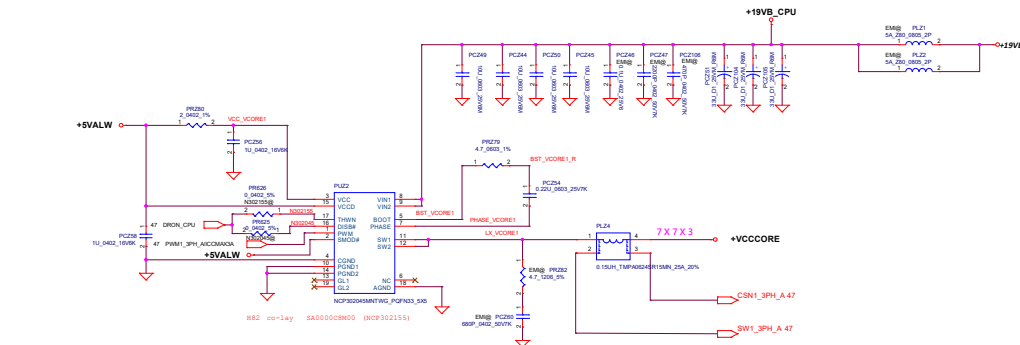


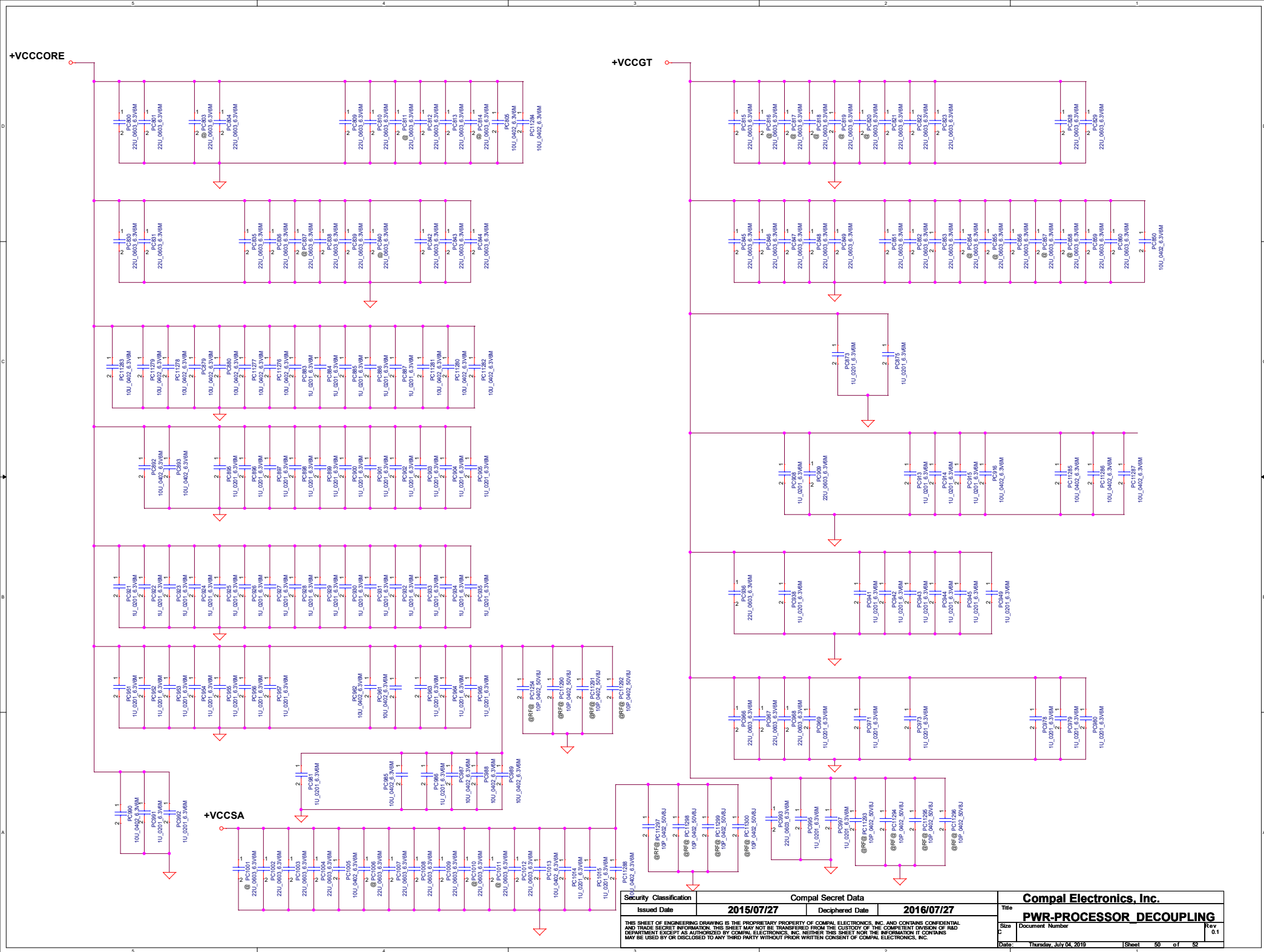












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